



60V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	2.0Ω @ V _G S = 5.0V	450mA
60V	2.5Ω @ $V_{GS} = 2.5V$	403mA
	4.0Ω @ V _{GS} = 1.8V	348mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Motor controls
- Power-management functions
- Backlighting

Features and Benefits

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

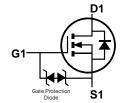
- Package: SOT563
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)

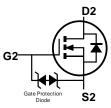


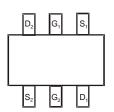




SOT563







Top View

Equivalent Circuit

Top View Pin out

Ordering Information (Note 4)

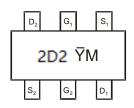
Part Number	Pookage	Packing		
Fait Number	Package	Qty.	Carrier	
DMN62D2UV-7	SOT563	3,000	Tape & Reel	
DMN62D2UV-13	SOT563	10,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



 $\begin{array}{l} 2D2 = \text{Product Type Marking Code} \\ \overline{\underline{Y}}M = \text{Date Code Marking} \\ \overline{Y} = \text{Year (ex: K = 2023)} \\ M = \text{Month (ex: 9 = September)} \end{array}$

Date Code Kev

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	М	N	Р	R	S	T	U	V	W
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	60	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) $V_{GS} = 5.0V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			lo	450 360	mA
Maximum Continuous Body Diode Forward Current (Note 6)			ls	450	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	%)		IDM	1.25	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.5	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	245	°C/W
Total Power Dissipation (Note 6)		P_{D}	0.8	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	156	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	ů

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:



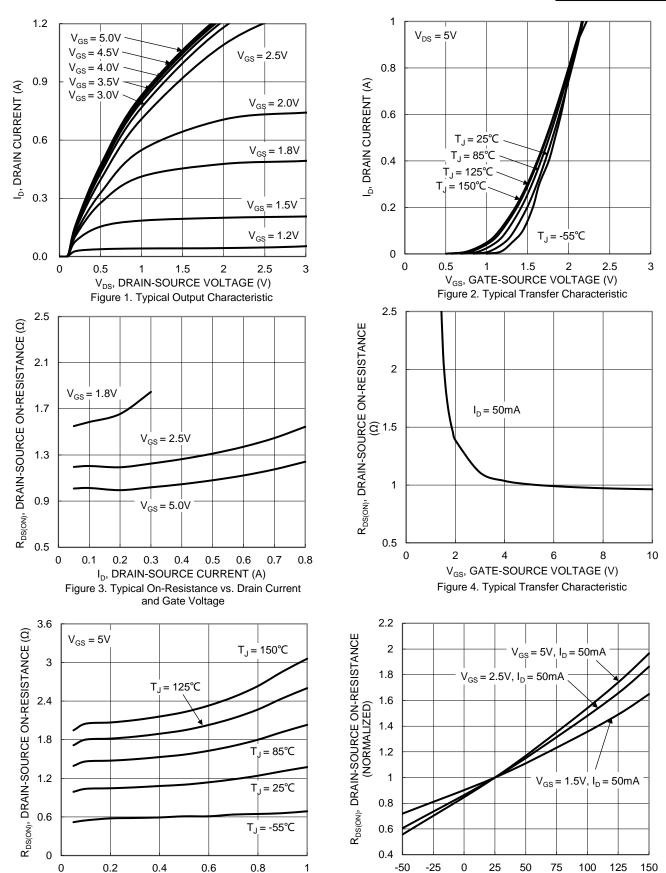
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	$V_{DS} = 60V$, $V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5		1.0	V	$V_{DS} = 10V, I_D = 250\mu A$
			1.0	2.0		$V_{GS} = 5.0V, I_{D} = 0.05A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.3	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$
			1.6	4.0		$V_{GS} = 1.8V, I_D = 0.05A$
Diode Forward Voltage	VsD	_	0.7	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	41	_	pF	\/ 20\/ \/ 0\/
Output Capacitance	Coss	_	5.4	_	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	4.2		рF	1 – 1.000112
Gate Resistance	R_g	_	52	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.8		nC	\/ 4.5\/ \/ 40\/
Gate-Source Charge	Qgs	_	0.2	_	nC	Vgs = 4.5V, Vps = 10V, Ip = 250mA
Gate-Drain Charge	Q_{gd}	_	0.1	_	nC	ID = 250IIIA
Turn-On Delay Time	td(ON)	_	1.5		ns	
Turn-On Rise Time	t _R	_	9.7	_	ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)	_	22.6	_	ns	$R_G = 25\Omega$, $I_D = 200mA$
Turn-Off Fall Time	tF	_	19.5	_	ns	

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.





0

0.4

I_D, DRAIN CURRENT (A)

Figure 5. Typical On-Resistance vs. Drain Current

and Junction Temperature

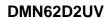
0.6

-50

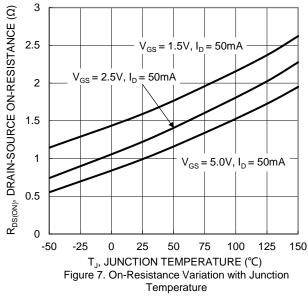
T_J, JUNCTION TEMPERATURE (°C)

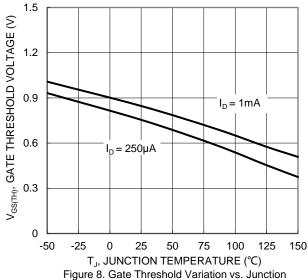
Figure 6. On-Resistance Variation with Junction

Temperature

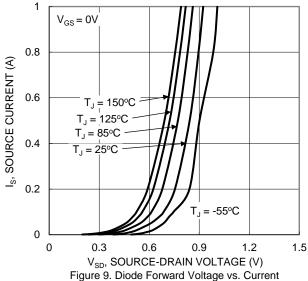


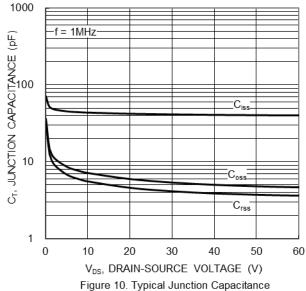


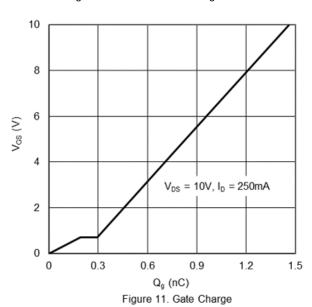


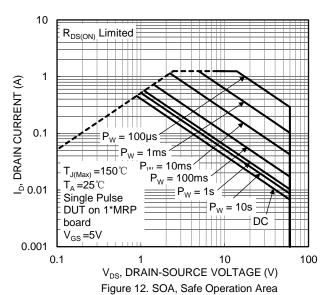


Temperature 1000









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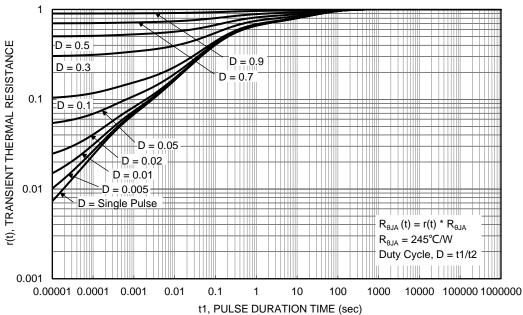


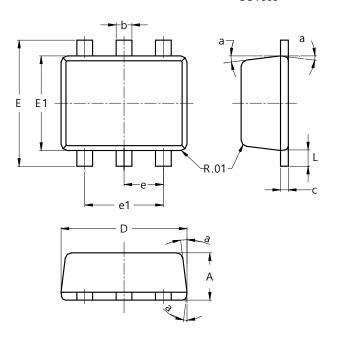
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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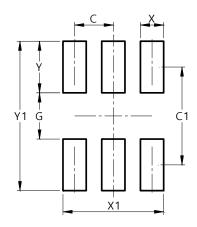


SOT563						
Dim	Min	Max	Тур			
Α	0.55	0.60				
b	0.15	0.30	0.20			
С	0.10	0.18	0.11			
D	1.50	1.70	1.60			
Е	1.55	1.70	1.60			
E1	1.10	1.25	1.20			
е			0.50			
e1	0.90	1.10	1.00			
L	0.10	0.30	0.20			
а	8°	9°	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT563



Dimensions	Value (in mm)
C	0.500
C1	1.270
G	0.600
Х	0.300
X1	1.300
Υ	0.670
Y1	1 940



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