



# 74LVT573

3.3 V octal D-type transparent latch; 3-state

Rev. 10 — 7 June 2024

Product data sheet

## 1. General description

The 74LVT573 is an 8-bit D-type transparent latch with 3-state outputs. The device features latch enable (LE) and output enable ( $\overline{OE}$ ) inputs. When LE is HIGH, data at the inputs enter the latches. In this condition the latches are transparent, a latch output will change each time its corresponding D-input changes. When LE is LOW the latches store the information that was present at the inputs a set-up time preceding the HIGH-to-LOW transition of LE. A HIGH on  $\overline{OE}$  causes the outputs to assume a high-impedance OFF-state. Operation of the  $\overline{OE}$  input does not affect the state of the latches. Bus hold data inputs eliminate the need for external pull-up resistors to define unused inputs

## 2. Features and benefits

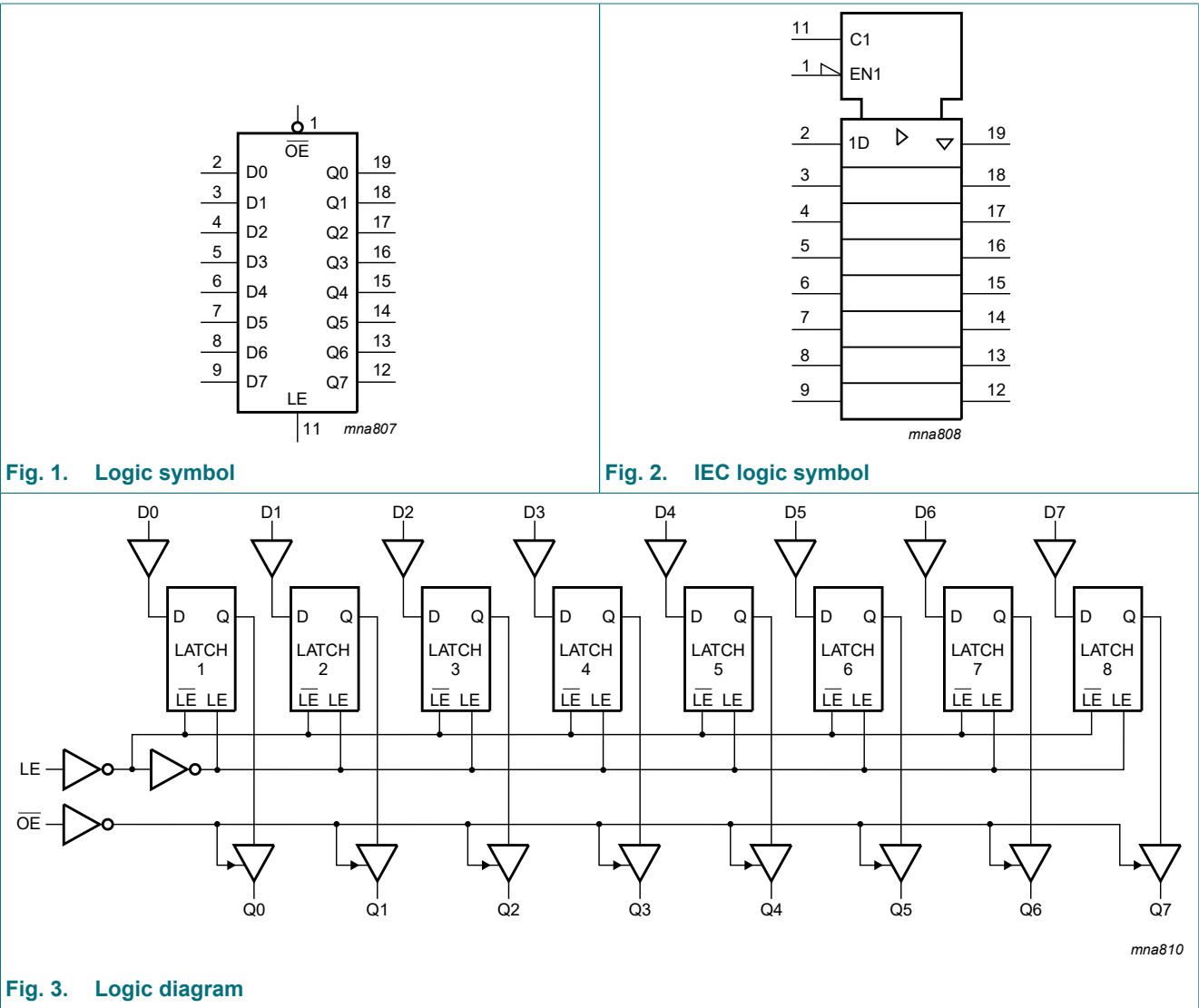
- Wide supply voltage range from 2.7 to 3.6 V
- Inputs and outputs arranged for easy interfacing to microprocessors
- 3-state outputs for bus interfacing
- Common output enable control
- Overvoltage tolerant inputs to 5.5 V
- BiCMOS high speed and output drive
- Direct interface with TTL levels
- Input and output interface capability to systems at 5 V supply
- Bus hold data inputs eliminate need for external pull-up resistors to hold unused inputs
- Live insertion and extraction permitted
- No bus current loading when output is tied to 5 V bus
- Power-up reset
- Power-up 3-state
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 500 mA per JESD 78 Class II Level B
- Complies with JEDEC standard JESD8C (2.7 V to 3.6 V)
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +85 °C

3. Ordering information

Table 1. Ordering information

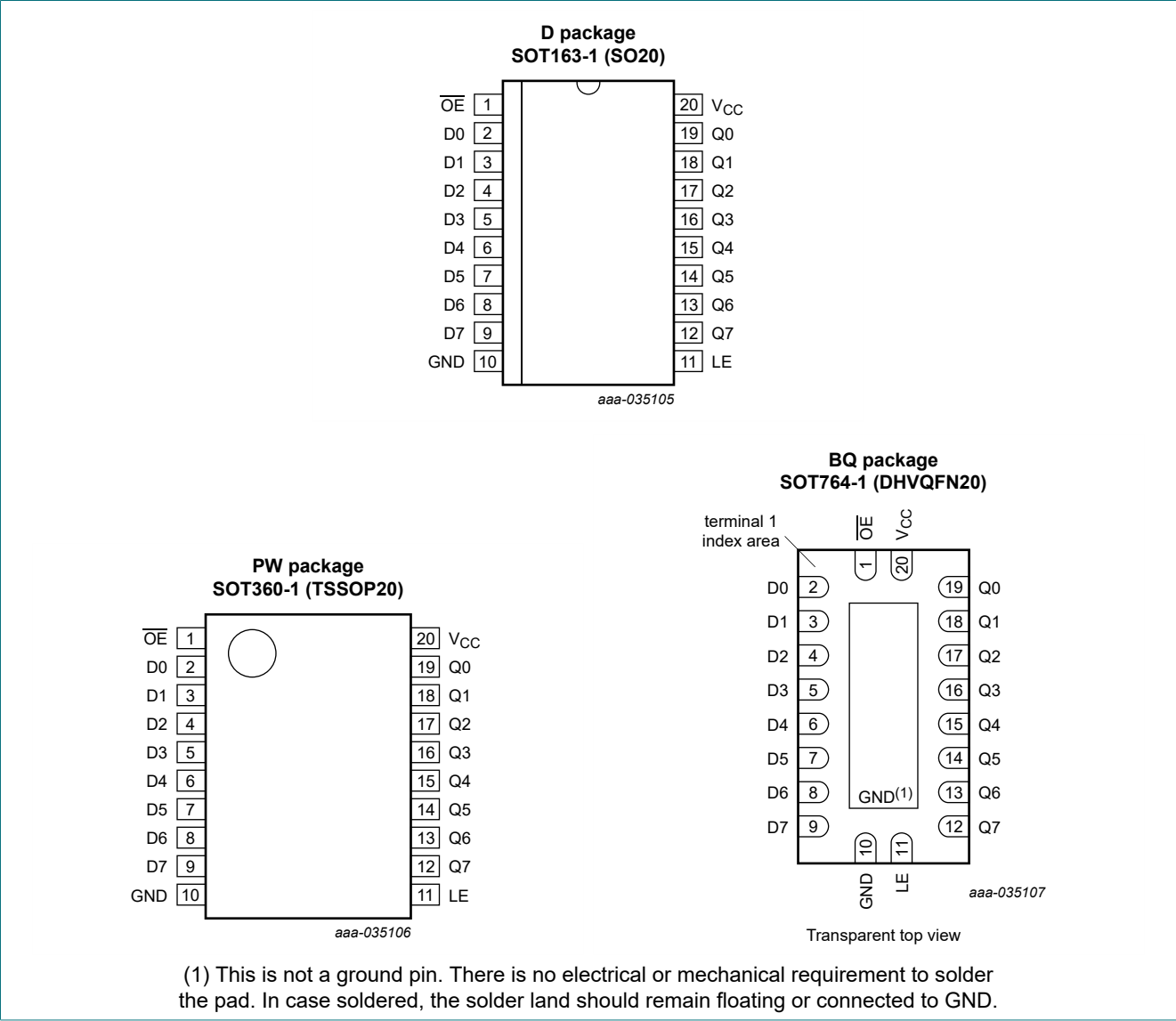
| Type number                | Package           |          |                                                                                                                                      |                          |
|----------------------------|-------------------|----------|--------------------------------------------------------------------------------------------------------------------------------------|--------------------------|
|                            | Temperature range | Name     | Description                                                                                                                          | Version                  |
| <a href="#">74LVT573D</a>  | -40 °C to +85 °C  | SO20     | plastic small outline package; 20 leads;<br>body width 7.5 mm                                                                        | <a href="#">SOT163-1</a> |
| <a href="#">74LVT573PW</a> | -40 °C to +85 °C  | TSSOP20  | plastic thin shrink small outline package; 20 leads;<br>body width 4.4 mm                                                            | <a href="#">SOT360-1</a> |
| <a href="#">74LVT573BQ</a> | -40 °C to +85 °C  | DHVQFN20 | plastic dual in-line compatible thermal enhanced<br>very thin quad flat package; no leads; 20 terminals;<br>body 2.5 × 4.5 × 0.85 mm | <a href="#">SOT764-1</a> |

4. Functional diagram



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

| Symbol                         | Pin                            | Description                      |
|--------------------------------|--------------------------------|----------------------------------|
| OE                             | 1                              | output enable input (active LOW) |
| D0, D1, D2, D3, D4, D5, D6, D7 | 2, 3, 4, 5, 6, 7, 8, 9         | data input                       |
| GND                            | 10                             | ground (0 V)                     |
| LE                             | 11                             | latch enable (active HIGH)       |
| Q0, Q1, Q2, Q3, Q4 ,Q5, Q6, Q7 | 19, 18, 17, 16, 15, 14, 13, 12 | data output                      |
| VCC                            | 20                             | supply voltage                   |

6. Functional description

Table 3. Function table

H = HIGH voltage level; h = HIGH voltage level one setup time prior to the LOW-to-HIGH clock transition;  
L = LOW voltage level; l = LOW voltage level one setup time prior to the LOW-to-HIGH clock transition;  
↓ = HIGH-to-LOW latch enable transition;  
Z = high-impedance OFF-state; NC = no change; X = don't care.

| Operating mode                | Control $\overline{OE}$ | Control LE | Input Dn | Internal register | Output Qn |
|-------------------------------|-------------------------|------------|----------|-------------------|-----------|
| Load and read register enable | L                       | H          | L        | L                 | L         |
|                               |                         |            | H        | H                 | H         |
| Latch and read register       | L                       | ↓          | l        | L                 | L         |
|                               |                         |            | h        | H                 | H         |
| Hold                          | L                       | L          | X        | NC                | NC        |
| Disable outputs               | H                       | L          | X        | NC                | Z         |
|                               |                         | H          | Dn       | Dn                | Z         |

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter               | Conditions                            | Min  | Max  | Unit |
|------------------|-------------------------|---------------------------------------|------|------|------|
| V <sub>CC</sub>  | supply voltage          |                                       | -0.5 | +4.6 | V    |
| V <sub>I</sub>   | input voltage           | [1]                                   | -0.5 | +7.0 | V    |
| V <sub>O</sub>   | output voltage          | output in OFF-state or HIGH-state [1] | -0.5 | +7.0 | V    |
| I <sub>IK</sub>  | input clamping current  | V <sub>I</sub> < 0 V                  | -    | -50  | mA   |
| I <sub>OK</sub>  | output clamping current | V <sub>O</sub> < 0 V                  | -    | -50  | mA   |
| I <sub>O</sub>   | output current          | output in LOW-state                   | -    | 128  | mA   |
|                  |                         | output in HIGH-state                  | -    | -64  | mA   |
| T <sub>stg</sub> | storage temperature     |                                       | -65  | +150 | °C   |
| T <sub>j</sub>   | junction temperature    | [2]                                   | -    | 150  | °C   |
| P <sub>tot</sub> | total power dissipation | T <sub>amb</sub> = -40 °C to +85 °C   | -    | 500  | mW   |

- [1] The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.
- [2] The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability.

8. Recommended operating conditions

Table 5. Recommended operating conditions

| Symbol           | Parameter                           | Conditions                                        | Min | Typ | Max | Unit |
|------------------|-------------------------------------|---------------------------------------------------|-----|-----|-----|------|
| V <sub>CC</sub>  | supply voltage                      |                                                   | 2.7 | -   | 3.6 | V    |
| V <sub>I</sub>   | input voltage                       |                                                   | 0   | -   | 5.5 | V    |
| V <sub>IH</sub>  | HIGH-level input voltage            |                                                   | 2.0 | -   | -   | V    |
| V <sub>IL</sub>  | LOW-level input voltage             |                                                   | -   | -   | 0.8 | V    |
| I <sub>OH</sub>  | HIGH-level output current           |                                                   | -   | -   | -32 | mA   |
| I <sub>OL</sub>  | LOW-level output current            |                                                   | -   | -   | 32  | mA   |
|                  |                                     | current duty cycle ≤ 50 %; f <sub>i</sub> ≥ 1 kHz | -   | -   | 64  | mA   |
| T <sub>amb</sub> | ambient temperature                 | in free air                                       | -40 | -   | +85 | °C   |
| Δt/ΔV            | input transition rise and fall rate | outputs enabled                                   | -   | -   | 10  | ns/V |

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol              | Parameter                         | Conditions                                                                                  | T <sub>amb</sub> = -40 °C to +85 °C |                       |      | Unit |
|---------------------|-----------------------------------|---------------------------------------------------------------------------------------------|-------------------------------------|-----------------------|------|------|
|                     |                                   |                                                                                             | Min                                 | Typ [1]               | Max  |      |
| V <sub>IK</sub>     | input clamping voltage            | V <sub>CC</sub> = 2.7 V; I <sub>IK</sub> = -18 mA                                           | -1.2                                | -0.9                  | -    | V    |
| V <sub>OH</sub>     | HIGH-level output voltage         | V <sub>CC</sub> = 2.7 V to 3.6 V; I <sub>OH</sub> = -100 μA                                 | V <sub>CC</sub> - 0.2               | V <sub>CC</sub> - 0.1 | -    | V    |
|                     |                                   | V <sub>CC</sub> = 2.7 V; I <sub>OH</sub> = -8 mA                                            | 2.4                                 | 2.5                   | -    | V    |
|                     |                                   | V <sub>CC</sub> = 3.0 V; I <sub>OH</sub> = -32 mA                                           | 2.0                                 | 2.2                   | -    | V    |
| V <sub>OL</sub>     | LOW-level output voltage          | V <sub>CC</sub> = 2.7 V; I <sub>OL</sub> = 100 μA                                           | -                                   | 0.1                   | 0.2  | V    |
|                     |                                   | V <sub>CC</sub> = 2.7 V; I <sub>OL</sub> = 24 mA                                            | -                                   | 0.3                   | 0.5  | V    |
|                     |                                   | V <sub>CC</sub> = 3.0 V I <sub>OL</sub> = 16 mA                                             | -                                   | 0.25                  | 0.4  | V    |
|                     |                                   | V <sub>CC</sub> = 3.0 V I <sub>OL</sub> = 32 mA                                             | -                                   | 0.3                   | 0.5  | V    |
|                     |                                   | V <sub>CC</sub> = 3.0 V I <sub>OL</sub> = 64 mA                                             | -                                   | 0.4                   | 0.55 | V    |
| V <sub>OL(pu)</sub> | power-up LOW-level output voltage | V <sub>CC</sub> = 3.6 V; I <sub>O</sub> = 1 mA; V <sub>I</sub> = GND or V <sub>CC</sub> [2] | -                                   | 0.13                  | 0.55 | V    |
| I <sub>I</sub>      | input leakage current             | all input pins;                                                                             |                                     |                       |      |      |
|                     |                                   | V <sub>CC</sub> = 0 V or 3.6 V; V <sub>I</sub> = 5.5 V                                      | -                                   | 1                     | 10   | μA   |
|                     |                                   | control pins;                                                                               |                                     |                       |      |      |
|                     |                                   | V <sub>CC</sub> = 3.6 V; V <sub>CC</sub> or GND                                             | -                                   | ±0.1                  | ±1   | μA   |
|                     |                                   | data pins                                                                                   |                                     |                       |      |      |
|                     |                                   | V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = V <sub>CC</sub> [3]                               | -                                   | 0.1                   | 1    | μA   |
| I <sub>OFF</sub>    | power-off leakage current         | V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = 0 V                                               | -5                                  | -1                    | -    | μA   |
|                     |                                   | V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = 0 V                                               | -                                   | -                     | -    | μA   |
| I <sub>OFF</sub>    | power-off leakage current         | V <sub>CC</sub> = 0 V; V <sub>I</sub> or V <sub>O</sub> = 0 V to 4.5 V                      | -                                   | 1                     | ±100 | μA   |
| I <sub>BHL</sub>    | bus hold LOW current              | Dn input; V <sub>CC</sub> = 3 V; V <sub>I</sub> = 0.8 V [4]                                 | 75                                  | 150                   | -    | μA   |
| I <sub>BHH</sub>    | bus hold HIGH current             | Dn input; V <sub>CC</sub> = 3 V; V <sub>I</sub> = 2.0 V                                     | -                                   | -150                  | -75  | μA   |
| I <sub>BHHO</sub>   | bus hold HIGH overdrive current   | Dn input; V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = 0 V to 3.6 V [4]                        | -                                   | -                     | 500  | μA   |
| I <sub>BHLO</sub>   | bus hold LOW overdrive current    | Dn input; V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = 0 V to 3.6 V                            | -500                                | -                     | -    | μA   |

3.3 V octal D-type transparent latch; 3-state

| Symbol                | Parameter                          | Conditions                                                                                                                         | T <sub>amb</sub> = -40 °C to +85 °C |         |      | Unit |
|-----------------------|------------------------------------|------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------|---------|------|------|
|                       |                                    |                                                                                                                                    | Min                                 | Typ [1] | Max  |      |
| I <sub>LO</sub>       | output leakage current             | Qn output HIGH when V <sub>O</sub> = 5.5 V and V <sub>CC</sub> = 3.0 V                                                             | -                                   | 60      | 125  | µA   |
| I <sub>O(pu/pd)</sub> | power-up/power-down output current | V <sub>CC</sub> ≤ 1.2 V; V <sub>O</sub> = 0.5 V to V <sub>CC</sub> ; V <sub>I</sub> = GND or V <sub>CC</sub> ; OE = don't care [5] | -                                   | 1       | ±100 | µA   |
| I <sub>OZ</sub>       | OFF-state output current           | V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                       |                                     |         |      |      |
|                       |                                    | output HIGH: V <sub>O</sub> = 3.0 V                                                                                                | -                                   | 1       | 5    | µA   |
|                       |                                    | output LOW: V <sub>O</sub> = 0.5 V                                                                                                 | -5                                  | -1      | -    | µA   |
| I <sub>CC</sub>       | supply current                     | V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A                                            |                                     |         |      |      |
|                       |                                    | outputs HIGH                                                                                                                       | -                                   | 0.13    | 0.19 | mA   |
|                       |                                    | outputs LOW                                                                                                                        | -                                   | 3       | 12   | mA   |
|                       |                                    | outputs disabled [6]                                                                                                               | -                                   | 0.13    | 0.19 | mA   |
| ΔI <sub>CC</sub>      | additional supply current          | per input pin; V <sub>CC</sub> = 3 V to 3.6 V; one input at V <sub>CC</sub> - 0.6 V and other inputs at V <sub>CC</sub> or GND [7] | -                                   | 0.1     | 0.2  | mA   |
| C <sub>I</sub>        | input capacitance                  | V <sub>I</sub> = 0 V or 3.0 V                                                                                                      | -                                   | 4       | -    | pF   |
| C <sub>O</sub>        | output capacitance                 | outputs disabled; V <sub>O</sub> = 0 V or 3.0 V                                                                                    | -                                   | 8       | -    | pF   |

[1] Typical values are measured at V<sub>CC</sub> = 3.3 V and T<sub>amb</sub> = 25 °C.

[2] For valid test results, data must not be loaded into the flip-flops (or latches) after applying power.

[3] Unused pins at V<sub>CC</sub> or GND.

[4] This is the bus hold overdrive current required to force the input to the opposite logic state.

[5] This parameter is valid for any V<sub>CC</sub> between 0 V and 1.2 V with a transition time of up to 10 ms.  
From V<sub>CC</sub> = 1.2 V to V<sub>CC</sub> = 3.3 V ± 0.3 V a transition time of 100 µs is permitted. This parameter is valid for T<sub>amb</sub> = 25 °C only.

[6] I<sub>CC</sub> is measured with outputs pulled to V<sub>CC</sub> or GND.

[7] This is the increase in supply current for each input at the specified voltage level other than V<sub>CC</sub> or GND.

10. Dynamic characteristics

Table 7. Dynamic characteristics  
Voltages are referenced to ground (GND = 0 V); for test circuit see Fig. 9.

| Symbol           | Parameter                           | Conditions                       | T <sub>amb</sub> = -40 °C to +85 °C |         |     | Unit |
|------------------|-------------------------------------|----------------------------------|-------------------------------------|---------|-----|------|
|                  |                                     |                                  | Min                                 | Typ [1] | Max |      |
| t <sub>PLH</sub> | LOW to HIGH propagation delay       | LE to Qn; see Fig. 4             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 1.6                                 | 3.5     | 5.6 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 6.3 | ns   |
|                  |                                     | Dn to Qn; see Fig. 5             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 1.0                                 | 2.5     | 4.2 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 4.7 | ns   |
| t <sub>PHL</sub> | HIGH to LOW propagation delay       | LE to Qn; see Fig. 4             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 2.5                                 | 4.3     | 6.5 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 7.2 | ns   |
|                  |                                     | Dn to Qn; see Fig. 5             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 1.0                                 | 2.7     | 4.3 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 5.2 | ns   |
| t <sub>PZH</sub> | OFF-state to HIGH propagation delay | OE to Qn; see Fig. 6             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 1.0                                 | 2.8     | 5.1 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 6.2 | ns   |
| t <sub>PZL</sub> | OFF-state to LOW propagation delay  | OE to Qn; see Fig. 7             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 1.3                                 | 3.3     | 5.5 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 6.6 | ns   |
| t <sub>PHZ</sub> | HIGH to OFF-state propagation delay | OE to Qn; see Fig. 6             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 2.0                                 | 3.7     | 5.7 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 6.7 | ns   |
| t <sub>PLZ</sub> | LOW to OFF-state propagation delay  | OE to Qn; see Fig. 7             |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 1.5                                 | 3.0     | 4.6 | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | -                                   | -       | 5.1 | ns   |
| t <sub>su</sub>  | set-up time                         | Dn to LE; see Fig. 8 [2]         |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 0.7                                 | -       | -   | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | 0.6                                 | -       | -   | ns   |
| t <sub>h</sub>   | hold time                           | Dn to LE; see Fig. 8 [3]         |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 1.6                                 | -       | -   | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | 1.8                                 | -       | -   | ns   |
| t <sub>w</sub>   | pulse width                         | LE input HIGH; see Fig. 4 [4]    |                                     |         |     |      |
|                  |                                     | V <sub>CC</sub> = 3.0 V to 3.6 V | 3.3                                 | -       | -   | ns   |
|                  |                                     | V <sub>CC</sub> = 2.7 V          | 3.3                                 | -       | -   | ns   |

[1] Typical values are at V<sub>CC</sub> = 3.3 V and T<sub>amb</sub> = 25 °C.  
[2] t<sub>su</sub> is the same as t<sub>su(L)</sub> and t<sub>su(H)</sub>.  
[3] t<sub>h</sub> is the same as t<sub>h(L)</sub> and t<sub>h(H)</sub>.  
[4] t<sub>w</sub> is the same as t<sub>wL</sub> and t<sub>wH</sub>.

10.1. Waveforms and test circuit

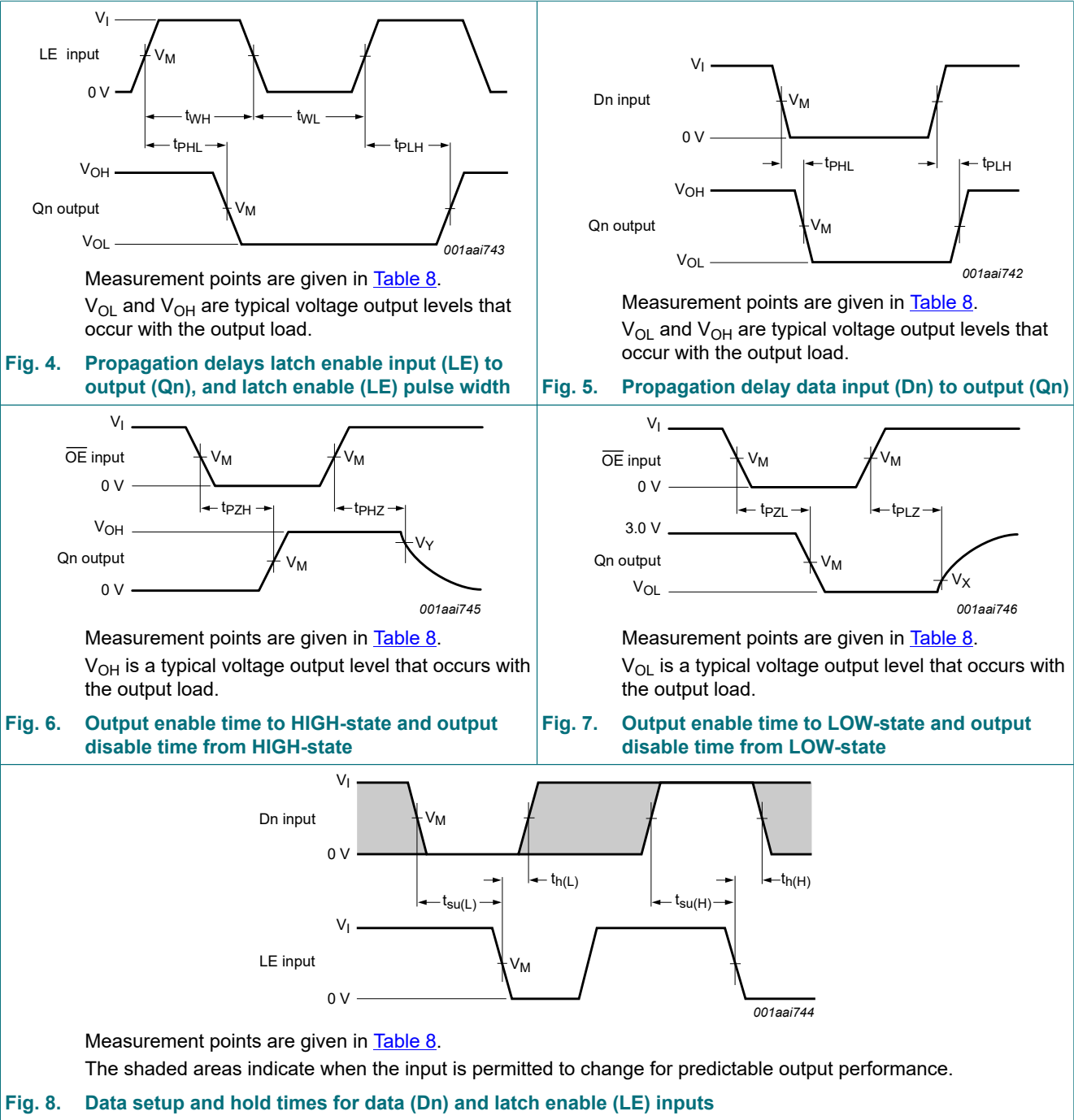


Table 8. Measurement points

| Input | Output |                         |                         |
|-------|--------|-------------------------|-------------------------|
| $V_M$ | $V_M$  | $V_X$                   | $V_Y$                   |
| 1.5 V | 1.5 V  | $V_{OL} + 0.3\text{ V}$ | $V_{OH} - 0.3\text{ V}$ |

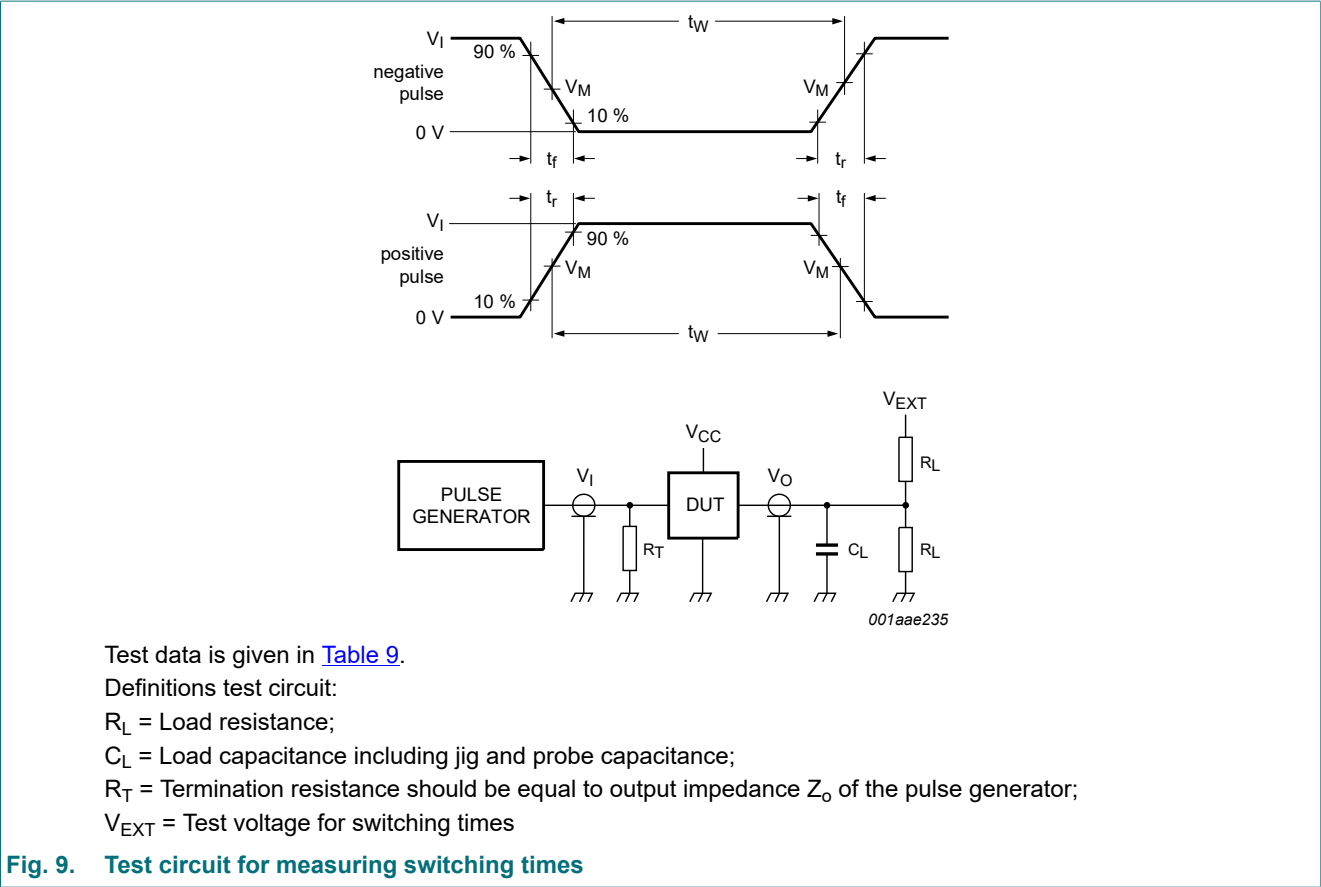


Fig. 9. Test circuit for measuring switching times

Table 9. Test data

| Input |               |        |               | Load  |              | $V_{EXT}$          |                    |                    |
|-------|---------------|--------|---------------|-------|--------------|--------------------|--------------------|--------------------|
| $V_I$ | $f_i$         | $t_W$  | $t_r, t_f$    | $C_L$ | $R_L$        | $t_{PHZ}, t_{PZH}$ | $t_{PLZ}, t_{PZL}$ | $t_{PLH}, t_{PHL}$ |
| 2.7 V | $\leq 10$ MHz | 500 ns | $\leq 2.5$ ns | 50 pF | 500 $\Omega$ | GND                | 6 V                | open               |

11. Package outline

SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1



Fig. 10. Package outline SOT163-1 (SO20)

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1



Fig. 11. Package outline SOT360-1 (TSSOP20)

DHVQFN20: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;  
20 terminals; body 2.5 x 4.5 x 0.85 mm

SOT764-1

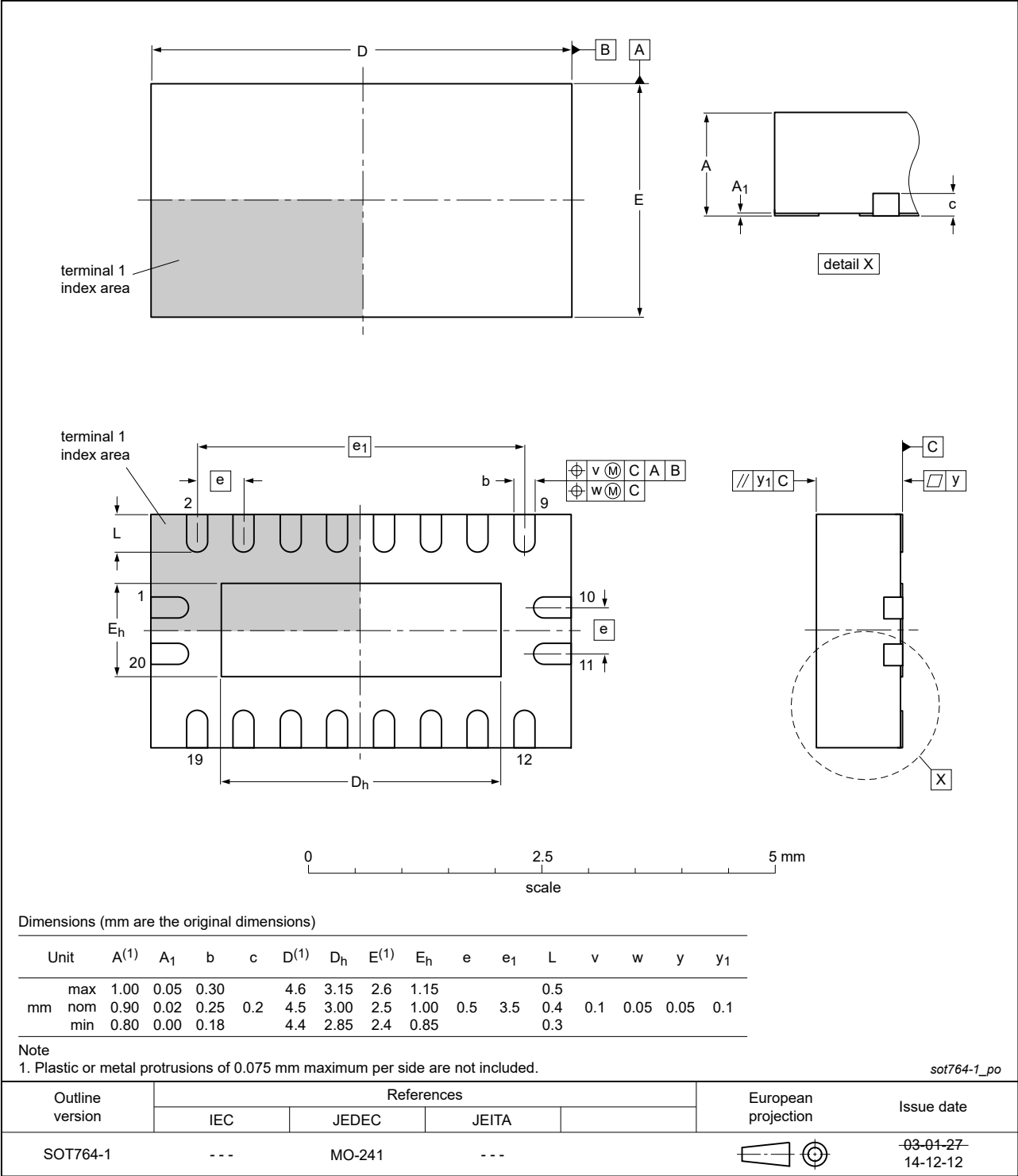


Fig. 12. Package outline SOT764-1 (DHVQFN20)

12. Abbreviations

Table 10. Abbreviations

| Acronym | Description                                     |
|---------|-------------------------------------------------|
| ANSI    | American National Standards Institute           |
| BiCMOS  | Bipolar Complementary Metal Oxide Semiconductor |
| CDM     | Charged Device Model                            |
| DUT     | Device Under Test                               |
| ESD     | ElectroStatic Discharge                         |
| ESDA    | ElectroStatic Discharge Association             |
| HBM     | Human Body Model                                |
| JEDEC   | Joint Electron Device Engineering Council       |
| TTL     | Transistor-Transistor Logic                     |

13. Revision history

Table 11. Revision history

| Document ID    | Release date                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | Data sheet status     | Change notice | Supersedes   |
|----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|---------------|--------------|
| 74LVT573 v.10  | 20240607                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.9 |
| Modifications: | <ul style="list-style-type: none"><li><a href="#">Section 2</a>: ESD specification updated according to the latest JEDEC standard.</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                            |                       |               |              |
| 74LVT573 v.9   | 20210730                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.8 |
| Modifications: | <ul style="list-style-type: none"><li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>Legal texts have been adapted to the new company name where appropriate.</li><li>Type number 74LVT573DB (SOT339-1/SSOP20) removed.</li><li><a href="#">Section 1</a> and <a href="#">Section 2</a> updated.</li><li><a href="#">Section 7</a>: Derating values for P<sub>tot</sub> total power dissipation removed.</li><li><a href="#">Fig. 12</a>: Package outline drawing SOT764-1 (DHVQFN20) updated.</li></ul> |                       |               |              |
| 74LVT573 v.8   | 20111122                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.7 |
| Modifications: | <ul style="list-style-type: none"><li>Legal pages updated.</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |                       |               |              |
| 74LVT573 v.7   | 20110912                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.6 |
| 74LVT573 v.6   | 20110727                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.5 |
| 74LVT573 v.5   | 20110629                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.4 |
| 74LVT573 v.4   | 20080915                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.3 |
| 74LVT573 v.3   | 20011217                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product data sheet    | -             | 74LVT573 v.2 |
| 74LVT573 v.2   | 19980219                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | Product specification | -             | -            |

## 14. Legal information

### Data sheet status

| Document status [1][2]         | Product status [3] | Definition                                                                            |
|--------------------------------|--------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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