



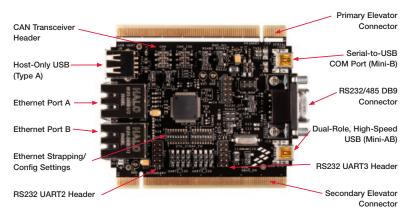
# TWR-SER2

Enhanced serial module





## Get to know the TWR-SER2





### TWR-SER2 Freescale Tower System

The TWR-SER2 module is part of the Freescale Tower System, a modular development platform that enables rapid prototyping and tool re-use through reconfigurable hardware. Take your design to the next level and begin constructing your Tower System today.

## **TWR-SER2** features

- Industrial Ethernet PHY configurable as:
  - o 10/100 dual-or single-port RMII
  - o 10/100 single-port MII
- Industrial High-Speed, Dual-Role USB (Host/Device) over UPLI
- Dedicated host mode USB port
- Four concurrent RS232 serial transceivers, including one S08JS16 based Serial-to-USB
- RS485 transceiver
- CAN transceiver



## **How to build your Tower**



## Locate the Flevator Modules

Each elevator module is identifiable by its four card edge connectors



### Identify the Elevator Modules

Each elevator module is either Primary or Secondary. They are identifiable as follows:

- Primary Elevator:
   Written on inside top and denoted
   by white card edge connectors
- Secondary Elevator:
   Written on inside bottom



### Locate Additional Modules

Gather any additional modules that will be used to assemble your desired Tower System configuration



# Identify the Primary and Secondary Card Edges

For each module, the words Primary and Secondary are written along the card edges. The Primary edge is denoted by a white stripe



# Plug in Primary Card Edge

Match the white stripe on the edge of each module to any available connector on the Primary Elevator and plug it in

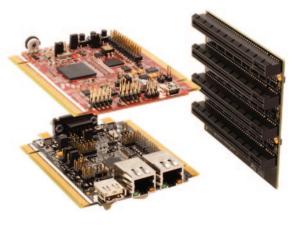


### Attach Secondary Elevator

With all desired modules connected to the Primary Elevator, carefully attach the Secondary Elevator onto the secondary card edges of each module







# Make Additional Connections

Plug in necessary cables and power sources

#### NOTE: First-generation modules

If using the first generation of TWR-ELEV modules, the board labeled Functional Elevator is equivalent to the Primary Elevator as described in the instructions above. The board labeled Dummy Elevator is different than the Secondary Elevator, but will work with most assembled Tower Systems.

If using first-generation peripheral and MCU/MPU modules, the white stripe along the outer edge of the board will not be present.



## **Configuration instructions**

In this Quick Start Guide, you will learn how to configure the various settings on the TWR-SER2 module for use in the Tower System.

### **Ethernet Settings**

Refer to these settings for typical Ethernet setting. For specific setting details refer to the TWR-SER2 Jumper Options section.

#### • 10/100 Dual RMII

- o J8 and J9 should be un-shunted
- $\circ\,$  Configure SW1 (1-8) as 11000000
- o Configure SW2 (1-8) as 10100000

### • 10/100 Signal-Port RMII

- o J9 should be un-shunted
- o Configure SW1 (1-8) as 11000000
- o Configure SW2 (1-8) as 10100000

### • 10/100 Signal-Port MII

- o J9 should be un-shunted
- o Configure SW1 (1-8) as 00110000
- o Configure SW2 (1-8) as 00010000

# High Speed Dual Role USB Settings

Refer to these settings to configure the High-Speed USB ULPI Transceiver.

- Host Mode—TWR-SER2 will be connected to a device, such as a USB memory stick using the appropriate mini USB adaptor.
  - o J24 should be un-shunted
- Device Mode—TWR-SER2 will be connected to a host, such as a host PC.
  - J24 should be shunted, if it is desired that the entire Tower system be powered via this connection

### **Host-Only USB Settings**

Refer to these settings to configure the Host-only USB. The TWR-SER2 will be connected to a device, such as a USB memory stick connected to the standard USB Type-A receptacle.

 J16 and J21 should be shunted if VBUS enable/over-current control is required



### **Serial Settings**

Refer to these settings to configure the multiple serial connections.

- Serial-to-USB—The TWR-SER2
  will be connected to a host PC using
  J5 (mini B). Use driver included on
  DVD to install the virtual COM port on
  the host PC.
  - o J7 pins 1-2, 3-4 should be shunted
- RS232 DB9—The TWR-SER2 will be connected to a host PC using the onboard DB9 connector.
  - o J1 pins 2-3 should be shunted
  - o J2 pins 2-3 should be shunted
  - o J13 pins 1-2 should be shunted
- RS485 DB9—The TWR-SER2 will be connected to a host PC using the onboard DB9 connector.
  - o J1 pins 1-2 should be shunted
  - J2 pins 1-2 should be shunted
  - J13 pins 2-3 should be shunted
  - Loopback settings can be configured using J11

- RS232 Headers—The TWR-SER2 will be connected a host PC using the required adapter cable(s) connected to J19 and/or J20.
  - J22 pins 1-2, 3-4 should be shunted to UART2 (J19)
  - J22 pins 5-6, 7-8 should be shunted if using RTS/CTS flow control on UART2 (J19)
  - J23 pins 1-2, 3-4 should be shunted to UART3 (J20)
  - J23 pins 5-6, 7-8 should be shunted if using RTS/CTS flow control on UART3 (J20)

### **CAN Settings**

Refer to these settings to enable the connection to the CAN Transceiver. The TWR-SER2 will be connected to a CAN device using J3. Refer to the TWR-SER2 User Manual for header pinout details.

- J4 pins 3-4, 5-6 should be connected
- J4 pins 1-2 can be connected to enable control of "Silent Mode"



## **TWR-SER2 Jumper Options**

The following is a list of all jumper options. The \*default\* installed jumper settings are shown in bold with asterisks.

	Option	Setting		Description
	Ethernet Strapping Functions / Settings	Dip 1	*0n*	Enables RMII mode for Ethernet PHY A
			Off	Enables MII mode for Ethernet PHY A
		Dip 2	*0n*	Connects RX_CRS to RMII0_CRS_DV (required for RMII operation)
			Off	Disconnects RX_CRS from RMIIO_CRS_DV (required for MII operation)
		Dip 3	0n	Connects RX_CRS to MII_CRS (required for MII operation)
			*0ff*	Disconnects RX_CRS from MII_CRS (required for RMII operation)
		Dip 4	0n	Connects RX_DV to MII_RXDV (required for MII operation)
5			*0ff*	Disconnects RX_DV from MII_RXDV (required for RMII operation)
SW1		Dip 5	0n	Enables Dual PHY Extender Mode
			*0ff*	Disables Extender Mode
		Dip 6	0n	PHY A - Auto Negotiation (Use ANO/AN1 to set highest capability)
			*0ff*	PHY A - Forced Mode (Use ANO/AN1 to set forced mode)
		Dip 7	0n	ANO_A - Full-Duplex on PHY A
			*0ff*	ANO_A - Half-Duplex on PHY A
		Dip 8	0n	AN1_A - 100Base-TX on PHY A
			*0ff*	AN1_A - 10Base-T on PHY A
	Ethernet Strapping Functions / Settings	Dip 1	*0n*	Enables RMII mode for Ethernet PHY B
			Off	Enables MII mode for Ethernet PHY B
SW2		Dip 2	On	Connects CLOCKOUTO to Ethernet PHY Clock
S			*0ff*	Isolates CLOCKOUTO from Ethernet PHY Clock
		Dip 3	*0n*	Connects onboard 50 MHz clock to Ethernet PHY Clock
			Off	Isolates onboard 50 MHz clock from Ethernet PHY Clock



	Option	Setting		Description
	Ethernet Strapping Functions/Settings	Dip 4	0n	Connects onboard 25 MHz clock to Ethernet PHY Clock
			*0ff*	Isolates onboard 25 MHz clock from Ethernet PHY Clock
		Dip 5	0n	Disables onboard 25 MHz/50 MHz clock
			*0ff*	Enables onboard 25 MHz/50 MHz clock
SW2		Dip 6	0n	PHY B - Auto Negotiation (Use ANO/AN1 to set highest capability)
S			*0ff*	PHY B - Forced Mode (Use ANO/AN1 to set forced mode)
		Dip 7	0n	ANO_B - Full-Duplex on PHY B
			*0ff*	ANO_B - Half-Duplex on PHY B
		Dip 8	0n	AN1_B - 100Base-TX on PHY B
			*0ff*	AN1_B - 10Base-T on PHY B
5	RS232/485 RX Select (UART 1) *2-3	2	RS485 Mode (connects RX to R0)	
		*2-3*		RS232 Mode (connects RX to R10UT)
	RS232/485 TX Select (UART 1)	1-2		RS485 Mode (connects TX to DI)
72		*2-3*		RS232 Mode (connects TX to T1IN)
	CAN Isolation	1-2		Connects CAN_S to S
J4		3-4		Connects CAN_TX to TXD
		5-6		Connects CAN_RX to RXD
	JS16 RS232 Isolation (UART 0)	*1-2*		Connects RX to S08JS16 RXD
7		*3-4*		Connects TX to S08JS16 TXD
96	Power Down Port B	1-2		Disables Ethernet PHY B



### **TWR-SER2 Jumper Options**

continued...

	Option	Setting	Description
60	Power Down Port A	1-2	Disables Ethernet PHY A
	RS485 Config (UART 1)	1-2	Loopback Mode (connects RE to DE)
		3-4	Loopback Mode (connects TX0_P to RX0_P)
=		5-6	Loopback Mode (connects TX0_N to RX0_N)
		7-8	NC
		9-10	5V Supply to DB9
3	RS232/485 Disable (UART 1)	*1-2*	Disables RS485
5		2-3	Disables RS232
116	VBUS OC Isolation	1-2	Connects USB VBUS OC to Elevator
J21	VBUS EN Isolation	1-2	Connects USB VBUS EN to Elevator
	RS232 (UART2) Isolation	1-2	Connects TX to T1IN
122		3-4	Connects RX to R10UT
25		5-6	Connects RTS to T2IN
		7-8	Connect CTS to R20UT
	RS232 (UART3) Isolation	1-2	Connects TX to T1IN
123		3-4	Connects RX to R10UT
3		5-6	Connects RTS to T2IN
		7-8	Connects CTS to R20UT
J24	USB Device Mode	1-2	Device Mode (capable of powering Tower System)





To learn more about the TWR-SER2 and other modules within the Tower System, go to **freescale.com/twrser2**. To become a member of the online Tower Geeks community, go to **towergeeks.org**.

Freescale, the Freescale logo and CodeWarrior are trademarks or registered trademarks of Freescale Semiconductor, Inc. Reg. U.S. Pat. & Tm. Off. All other product or service names are the property of their respective owners.

© 2010 Freescale Semiconductor, Inc.

Doc Number: TWRSER2QSG / REV 0 Agile Number: 926-78501 / REV A

