AM25S557, AM25S558

Eight-Bit by Eight-Bit Combinatorial Multiplier

The AM25S557 and AM25S558 are high-speed, combinatorial, 8 x 8-bit multipliers. Both use an array of full adders to form and add partial products in a single unclocked operation, resulting in a 16-bit parallel output product.

Mode control inputs X_M and Y_M allow the multiplier to accept either unsigned or two's complement numbers from either respective input to provide an unsigned or signed output. The mode control lines are held LOW for unsigned input words and HIGH for two's complement.

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
    - Class Q Military
    - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
    - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

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Am25S557/Am25S558

Eight-Bit by Eight-Bit Combinatorial Multiplier

DISTINCTIVE CHARACTERISTICS

- Multiplies two 8-bit numbers – 16-bit output
- Combinatorial – no clocks required
- Full 8 x 8 multiply in 45ns typ.
- Cascades to 16 x 16 in 110ns typ.
- Expandable to multiples of 8 bits
- MSB and $\overline{MSB}$ outputs for easy expansion
- Unsigned, two's complement or mixed operands
- Implements common rounding algorithms with additional logic
- Three-state outputs
- Transparent 16-bit latch in Am25S557
- Industry standard pin-outs

GENERAL DESCRIPTION

The Am25S557 and Am25S558 are high-speed, combinatorial, 8 x 8-bit multipliers. Both use an array of full adders to form and add partial products in a single unclocked operation, resulting in a 16-bit parallel output product.

Mode control inputs X_M and Y_M allow the multiplier to accept either unsigned or two's complement numbers from either respective input to provide an unsigned or signed output. The mode control lines are held LOW for unsigned input words and HIGH for two's complement.

The Am25S557 and Am25S558 are easily expandable to longer work lengths. Both S_{15} and $\overline{S}_{15}$ are available to allow expansion in either signed or unsigned modes without external inverters. In the 16-bit by 16-bit configuration (32-bit output) the typical multiply time is 110ns.

Both configurations offer three-state output flexibility and the Am25S557 adds a 16-bit transparent latch between the multiplier array and the three-state output buffers (including $\overline{S}_{15}$).

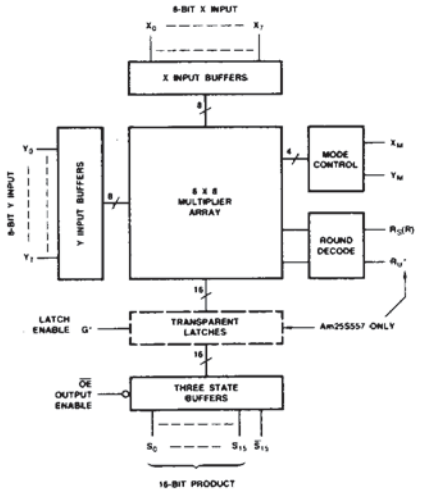
Rounding provisions for 8-bit truncated output configurations are particularly optimized for maximum flexibility. The Am25S557 internally develops proper rounding for either signed or unsigned numbers by combining rounding input R with X_M, Y_M, $\overline{X}_M$ and $\overline{Y}_M$ as follows:

$R_U = \overline{X}_M \cdot \overline{Y}_M \cdot R$ = Unsigned Rounding input to 2^7 adder.

$R_S = (X_M + Y_M)\ R$ = Signed Rounding input to 2^6 adder.

Since the Am25S558 does not require the use of pin 9 for the latch enable input, (G), R_S and R_U are brought out separately.

BLOCK DIAGRAM



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*Pin 11 is G for Am25S557 and R_U for Am25S558.

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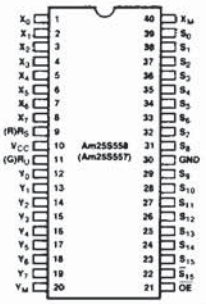
CONNECTION DIAGRAM
Top View

D-40-1



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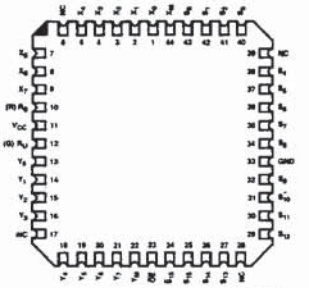

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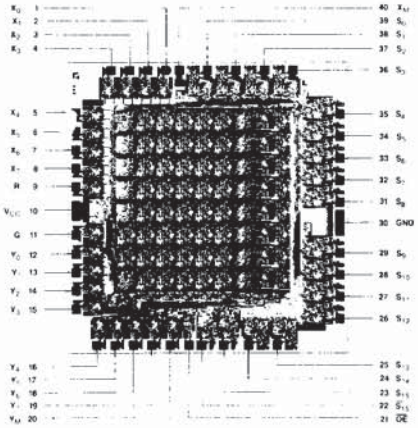
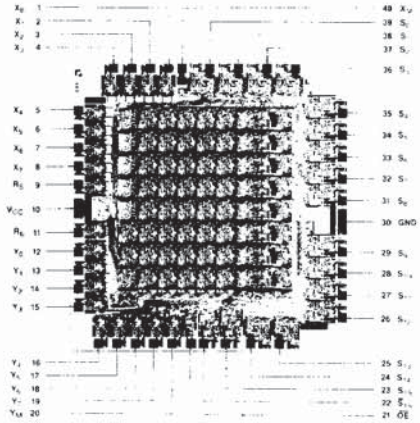
Pin assignments shown are for Am25S558. G and R shown in parentheses are pin assignments for Am25S557.

METALLIZATION AND PAD LAYOUT

Am25S557



Am25S558



DIE SIZE 0.171" x 0.165"

ORDERING INFORMATION

AMD products are available in several packages and operating ranges. The order number is formed by a combination of the following: Device number, speed option (if applicable), package type, operating range and screening option (if desired).

Am25S557
Am25S558

D C B

- Screening Option
  - Blank – Standard processing
  - B – Burn-in
- Temperature (See Operating Range)
  - C – Commercial (0°C to +70°C)
  - M – Military (–55°C to +125°C)
- Package
  - D – 40-pin CERDIP
  - L – 44-pin leadless chip carrier
  - X – Dice
- Device type
  - 8-Bit by 8-Bit Multiplier

Valid Combinations	
Am25S557	DC, DM LC, LM
Am25S558	DC, DM LC, LM

Valid Combinations

Consult the AMD sales office in your area to determine if a device is currently available in the combination you wish.

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PIN DESCRIPTION

Pin No.	Name	I/O	Description
	$X_0 - X_7$	I	Multiplicand 8-bit data inputs.
	$Y_0 - Y_7$	I	Multiplier 8-bit data inputs.
	X_M, Y_M	I	Mode control inputs for each data word; LOW for unsigned data and HIGH for two's complement data.
	$S_0 - S_{15}$	O	Product 16-bit output.
23	$\overline{S}_{15}$		Inverted MSB for expansion.
9, 11	R_S, R_U	I	Rounding inputs for signed and unsigned data, respectively (Am25S558 only).
11	G		Transparent Latch Enable (Am25S557 only).
21	$\overline{OE}$	O	Three-state enable for S_0–S_{15} outputs.
9	R	I	Rounding input for signed or unsigned data (combined internally with X_M, Y_M in Am25S557 only).

MODE CONTROL INPUTS

Operating Mode	Input Data		Mode Control Inputs	
	$X_0 - X_7$	$Y_0 - Y_7$	X_M	Y_M
UNSIGNED	UNSIGNED	UNSIGNED	L	L
MIXED	UNSIGNED	2's COMP	L	H
	2's COMP	UNSIGNED	H	L
SIGNED	2's COMP	2's COMP	H	H

ROUNDING INPUTS
Am25S557

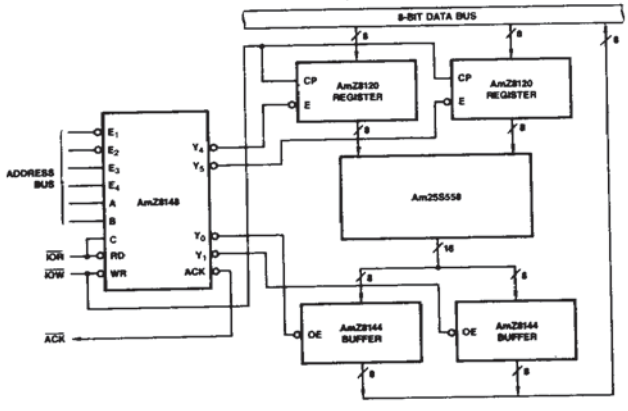
Inputs			Adds	
X_M	Y_M	R	2^7	2^6
L	L	H	YES	NO
L	H	H	NO	YES
H	L	H	NO	YES
H	H	H	NO	YES
X	X	L	NO	NO

Am25S558

Inputs		Adds		Normally Used With	
R_U	R_S	2^7	2^6	X_M	Y_M
L	L	NO	NO	X	X
L	H	NO	YES	$X_M + Y_M = H$	
H	L	YES	NO	L	L
H	H	YES	YES	*	*

* Most rounding applications require a HIGH level for R_U or R_S, but not both.

I/O MAPPED INTERFACE WITH MOS MICROPROCESSOR



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Refer to Page 13-1 for Essential Information on Military Devices

APPLICATION

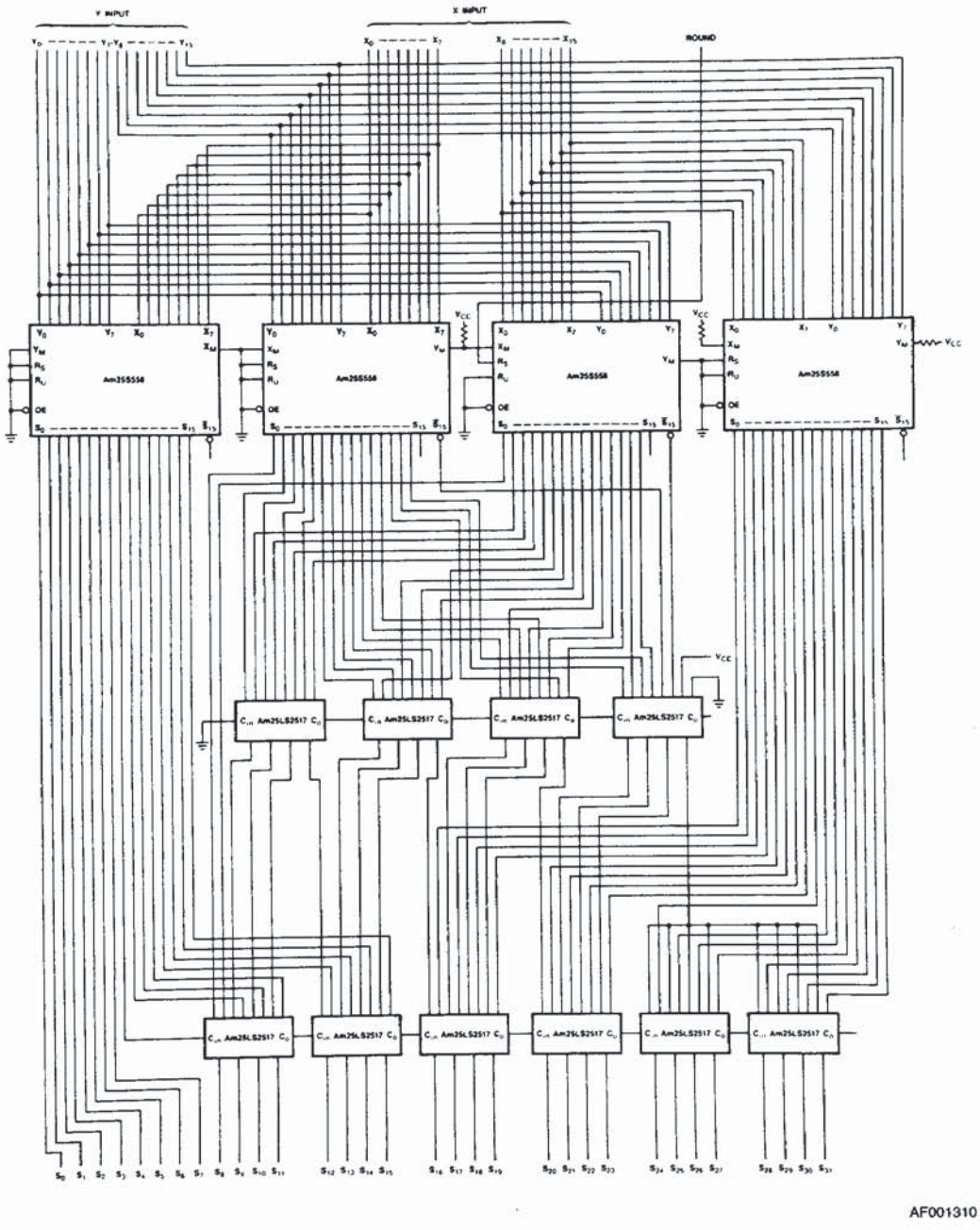

Figure 1. High-Speed 16 x 16 2's Complement Multiplication.

ABSOLUTE MAXIMUM RATINGS

Storage Temperature ..... −65°C to +150°C
Ambient Temperature Under Bias ..... −55°C to +125°C
Supply Voltage to Ground Potential
Continuous ..... −0.5V to +7.0V
DC Voltage Applied to Outputs For
High Output State ..... −0.5V to +V_{CC} max
DC Input Voltage ..... −0.5V to +5.5V
DC Output Current, Into Outputs 30mA
DC Input Current ..... −30mA to +5.0mA

Stresses above those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent device failure. Functionality at or above these limits is not implied. Exposure to absolute maximum ratings for extended periods may affect device reliability.

OPERATING RANGES

Commercial (C) Devices
Temperature ..... 0°C to +70°C
Supply Voltage ..... +4.75V to +5.25V

Military (M) Devices
Temperature ..... −55°C to +125°C
Supply Voltage ..... +4.5V to +5.5V

Operating ranges define those limits over which the functionality of the device is guaranteed.

DC CHARACTERISTICS over operating range unless otherwise specified

Parameters	Description	Test Conditions (Note 2)			Min	Typ (Note 1)	Max	Units
V_{OH}	Output HIGH Voltage	V_{CC} = MIN, V_{IN} = V_{IH} or V_{IL}	V_{IL} = 0.8V, V_{IH} = 2.0V	I_{OH} = −2.0mA	2.4	3.0		Volts
V_{OL}	Output LOW Voltage	V_{CC} = MIN, V_{IN} = V_{IH} or V_{IL}	V_{IL} = 0.8V, V_{IH} = 2.0V, I_{OL} = 8.0mA			0.3	0.5	Volts
V_{IH}	Input HIGH Level	Guaranteed input logical HIGH voltage for all inputs			2.0			Volts
V_{IL}	Input LOW Level	Guaranteed input logical LOW voltage for all inputs		MIL			0.8	Volts
				COM'L			0.8	
V_I	Input Clamp Voltage	V_{CC} = MIN, I_{IN} = −18mA					−1.5	Volts
I_{IL}	Input LOW Current	V_{CC} = MAX, V_{IN} = 0.5V					−1.0	mA
I_{IH}	Input HIGH Current	V_{CC} = MAX, V_{IN} = 2.4V					100	µA
I_I	Input HIGH Current	V_{CC} = MAX, V_{IN} = 5.5V					1	mA
I_O	Off State (High Impedance) Output Current	V_{CC} = MAX		V_O = 0.5V			−100	µA
				V_O = 2.4V			+100	
I_{SC}	Output Short Circuit Current (Note 3)	V_{CC} = MAX.			−20		−90	mA
I_{CC}	Power Supply Current (Note 4)	V_{CC} = MAX					280	mA

Notes: 1. Typical limits are at V_{CC} = 5.0V, 25°C ambient and maximum loading.
2. For conditions shown as MIN or MAX, use the appropriate value specified under Operating Ranges for the applicable device type.
3. Not more than one output should be shorted at a time. Duration of the short circuit test should not exceed one second.
4. Test with pin 21 at 4.5V, all other input pins at GND, all outputs open Am25S557 conditions the same except initialize with G (pin 11) at 4.5V, then GND.

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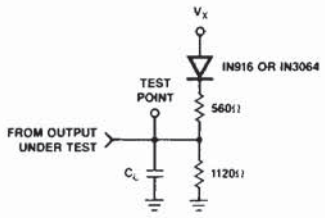
Refer to Page 13-1 for Essential Information on Military Devices

SWITCHING TEST CIRCUIT



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SWITCHING TEST WAVEFORMS

Test	V_X	Output Waveform – Measurement Level
All t_{PD}s	5.0V	V_{OH}, V_{OL}, 1.5V
t_{PHZ}	0.0V	V_{OH}, 0.5V, 0.0V
t_{PLZ}	5.0V	2.8V, V_{OL}, 0.5V
t_{PZH}	0.0V	V_{OH}, 1.5V, 0.0V
t_{PZL}	5.0V	2.8V, 1.5V, V_{OL}

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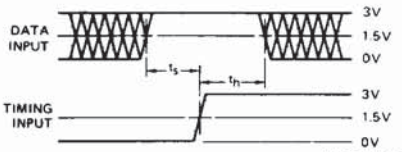
C_L Includes probe and jig capacitance.

SET-UP AND HOLD TIMES



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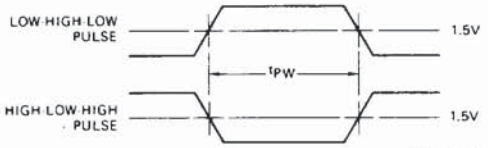
PULSE WIDTH



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Notes:1. Diagram shown for HIGH data only. Output transition may be opposite sense.
2. Cross hatched area is don't care condition.

SWITCHING CHARACTERISTICS over operating range unless otherwise specified*

Parameters	Description	Test Conditions	COMMERCIAL Am25S557 Min	COMMERCIAL Am25S557 Typ	COMMERCIAL Am25S557 Max	MILITARY Am25S557 Min	MILITARY Am25S557 Typ	MILITARY Am25S557 Max	Units
t_{PD}	X_i, Y_i to S_0 to S_7	C_L = 30pF, R_L = 560Ω (See test figures)		45	60		55	70	ns
t_{PD}	X_i, Y_i to S_8 to S_{15} or $\overline{S}_{15}$			50	80		60	90	ns
t_s	X_i, Y_i to G Set-up Time		65			75			ns
t_h	X_i, Y_i to G Hold Time		−5			−5			ns
t_{PD}	G to S_1			30	45		30	50	ns
t_{PW}	Latch Enable Pulse Width		25	15		30	15		ns
t_{PHZ}	$\overline{OE}$ to S_0 to S_{15}			15	30		15	40	ns
t_{PHZ}	$\overline{OE}$ to $\overline{S}_{15}$			25	40		25	50	ns
t_{PLZ}	$\overline{OE}$ to S_1			15	30		15	40	ns
t_{PZH}	$\overline{OE}$ to S_1			20	35		20	40	ns
t_{PZL}	$\overline{OE}$ to S_1			20	35		20	40	ns

*AC performance over the operating temperature range is guaranteed by testing defined in Group A, Subgroup 9.

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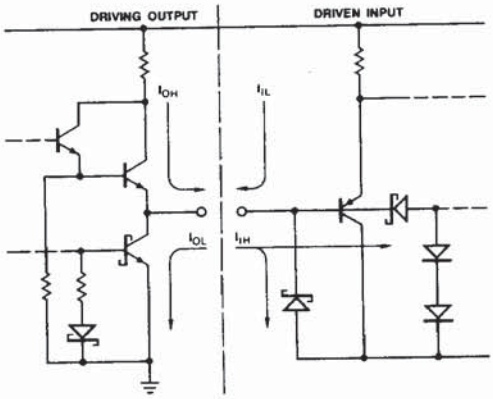
Refer to Page 13-1 for Essential Information on Military Devices

SWITCHING CHARACTERISTICS over operating range unless otherwise specified*

Parameters	Description	Test Conditions	COMMERCIAL Am25S558 Min	Typ	Max	MILITARY Am25S558 Min	Typ	Max	Units
t_{PD}	X_1, Y_1 to S_0 to S_7	C_L = 30pF, R_L = 580Ω (See test figures)		35	55		35	65	ns
t_{PD}	X_1, Y_1 to S_0 to S_{15} or $\overline{S}_{15}$			55	75		55	85	ns
t_{PHZ}	$\overline{OE}$ to S_0 to S_{15}			15	30		15	40	ns
t_{PHZ}	$\overline{OE}$ to $\overline{S}_{15}$			25	40		25	50	ns
t_{PLZ}	$\overline{OE}$ to S_1			15	30		15	40	ns
t_{PZH}	$\overline{OE}$ to S_1			20	35		20	40	ns
t_{PZL}	$\overline{OE}$ to S_1			20	35		20	40	ns

*AC performance over the operating temperature range is guaranteed by testing defined in Group A, Subgroup 9.

Am25S557/Am25S558 INPUT/OUTPUT CURRENT INTERFACE CONDITIONS



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RELATED PRODUCTS

Part No.	Description
Am29516/7	16 by 16-Bit Multiplier
Am25S05	4 by 2-Bit Multiplier
Am25LS14A	8-Bit Serial/Parallel Multiplier

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Refer to Page 13-1 for Essential Information on Military Devices