

# PART NUMBER 54123WB-ROCA

# Rochester Electronics Manufactured Components

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### **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
  - Class Q Military
  - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

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The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



# DM54123, DM74123

# Dual Retriggerable One-Shot with Clear and Complementary Outputs

The '123 is a dual retriggerable monostable multivibrator capable of generating output pulses from a few nano-seconds to extremely long duration up to 100% duty cycle. Each device has three inputs permitting the choice of either leading-edge or trailing edge triggering. Pin (A) is an active-low transition trigger input and pin (B) is an active-high transition trigger input. A low at the clear (CLR) input terminates the output pulse: which also inhibits triggering. An internal connection from CLR to the input gate makes it possible to trigger the circuit by a positive-going signal on the CLR.

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DM54123/DM74123 Dual Retriggerable One-Shot with Clear and Complementary Outputs



# DM54123/DM74123 Dual Retriggerable One-Shot with **Clear and Complementary Outputs**

#### General Description

The '123 is a dual retriggerable monostable multivibrator capable of generating output pulses from a few nano-seconds to extremely long duration up to 100% duty cycle. Each device has three inputs permitting the choice of either leading-edge or trailing edge triggering. Pin (A) is an activelow transition trigger input and pin (B) is an active-high transition trigger input. A low at the clear (CLR) input terminates the output pulse: which also inhibits triggering. An internal connection from CLR to the input gate makes it possible to trigger the circuit by a positive-going signal on CLR as shown in the truth table.

To obtain the best and trouble free operation from this device please read the operating rules as well as the NSC one-shot application notes carefully and observe recommendations.

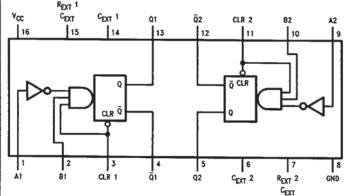
- DC triggered from active-high transition or active-low transition inputs
- Retriggerable to 100% duty cycle
- Direct reset terminates output pulse
- Compensated for V<sub>CC</sub> and temperature variations
- DTL, TTL compatible
- Input clamp diodes

#### **Functional Description**

The basic output pulse width is determined by selection of an external resistor (Rx) and capacitor (Cx). Once triggered, the basic pulse width may be extended by retriggering the gated active-low transition or active-high transition inputs or be reduced by use of the active-low transition clear input. Retriggering to 100% duty cycle is possible by application of an input pulse train whose cycle time is shorter than the output cycle time such that a continuous "HIGH" logic state is maintained at the "Q" output.

#### Connection Diagram

#### **Dual-In-Line Package**



#### TL/F/6539-1

Order Number DM54123J-MIL, DM54123W-MIL or DM74123N See NS Package Number J16A, N16A or W16A

#### **Triggering Truth Table**

|   | Inputs        | Response |            |  |  |
|---|---------------|----------|------------|--|--|
| A | В             | CLR      | Mosponso   |  |  |
| X | X             | L        | No Trigger |  |  |
|   | L             | X        | No Trigger |  |  |
| ~ | н             | Н        | Trigger    |  |  |
| н | $\mathcal{L}$ | X        | No Trigger |  |  |
| L | $\mathcal{L}$ | Н        | Trigger    |  |  |
| L | Н             | _        | Trigger    |  |  |

- H = HIGH Voltage Level
- L = LOW Voltage Level
- X = Immaterial

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### Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 5.5V

Operating Free Air Temperature Range

Storage Temperature -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

#### **Recommended Operating Conditions**

| Symbol                   | Parameter   |             | DM54123 |     |      | DM74123        |  |      | I I I I I I |
|--------------------------|---|-------------|---------|-----|------|----------------|--|------|-------------|
|                          |   |             | Min     | Nom | Max  | Min            | Nom  | Max  | Units       |
| Vcc                      | Supply Voltage  |             | 4.5     | 5   | 5.5  | 4.75           | 5  | 5.25 | V           |
| VIH                      | High Level Input Voltage  |             | 2       |     |      | 2              |  |      | v           |
| V <sub>IL</sub>          | Low Level Input Voltage   |             |         |     | 0.8  |                |  | 0.8  | v           |
| Іон                      | High Level Output Current   |             |         |     | -0.8 |                |  | -0.8 | mA          |
| loL                      | Low Level Output Current  |             |         |     | 16   |                | <del>                                     </del> | 16   | mA          |
| t <sub>W</sub>           | Pulse Width<br>(Note 5)   | A or B High |         |     |      | 40             |  |      | ns          |
|                          |   | A or B Low  |         |     |      | 40             |  |      |             |
|                          |   | Clear Low   |         |     |      | 40             |  |      |             |
| T <sub>WQ</sub><br>(Min) | Minimum Width of<br>Pulse at Q (Note 5)                                       | A or B      |         |     | 80   |                |  | 65   | ns          |
| R <sub>EXT</sub>         | External Timing Resistor  |             |         |     |      | 5              |  | 50   | kΩ          |
| C <sub>EXT</sub>         | External Timing Capacitance   |             |         |     |      | No Restriction |  | μF   |             |
| C <sub>WIRE</sub>        | Wiring Capacitance<br>at R <sub>EXT</sub> /C <sub>EXT</sub> Terminal (Note 5) |             |         |     |      |                |  | 50   | pF          |
| TA                       | Free Air Operating Temperature  |             | -55     |     | 125  | 0              |  | 70   | °C          |

#### **Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

| Symbol          | Parameter                            | Conditions   |       | Min | Typ<br>(Note 1) | Max  | Units |
|-----------------|--------------------------------------|--|-------|-----|-----------------|------|-------|
| VI              | Input Clamp Voltage                  | $V_{CC} = Min, I_1 = -12 \text{ mA}$   |       |     |                 | -1.5 | V     |
| V <sub>OH</sub> | High Level Output<br>Voltage         | V <sub>CC</sub> = Min, I <sub>OH</sub> = Max   | DM54  | 2.4 | 2.4             |      |       |
|                 |                                      | V <sub>IL</sub> = Max, V <sub>IH</sub> = Min   | 2.5   | 3.4 |                 | \ \  |       |
| V <sub>OL</sub> | Low Level Output<br>Voltage          | V <sub>CC</sub> = Min, I <sub>OL</sub> = Max<br>V <sub>IH</sub> = Min, V <sub>IL</sub> = Max |       |     | 0.2             | 0.4  | v     |
| l <sub>l</sub>  | Input Current @ Max<br>Input Voltage | V <sub>CC</sub> = Max, V <sub>I</sub> = 5.5V   |       |     |                 | 1    | mA    |
| l <sub>IH</sub> | High Level Input                     | V <sub>CC</sub> = Max  | Data  |     |                 | 40   |       |
|                 | Current                              | $V_I = 2.4V$   | Clear |     |                 | 80   | μА    |
| lir l           | Low Level Input Current              | $V_{CC} = Max, V_I = 0.4V$   | Clear |     |                 | -3.2 | mA    |
|                 |                                      |  | Data  |     |                 | -1.6 |       |
| los             | Short Circuit<br>Output Current      | V <sub>CC</sub> = Max  | DM54  | -10 |                 | -40  |       |
|                 |                                      | (Note 2)   | DM74  | -10 |                 | -40  | mA    |
| lcc             | Supply Current                       | V <sub>CC</sub> = Max (Notes 3 and 4)  |       |     | 46              | 66   | mA    |

Note 1: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

Note 2: Not more than one output should be shorted at a time.

Note 3: Quiescent I<sub>CC</sub> is measured (after clearing) with 2.4V applied to all clear and A inputs, B inputs grounded, all outputs open,  $C_{EXT} = 0.02 \ \mu\text{F}$ , and  $R_{EXT} = 25 \ \text{K}\Omega$ .

Note 4:  $I_{CC}$  is measured in the triggered state with 2.4V applied to all clear and B inputs, A inputs grounded, all outputs open,  $C_{EXT}=0.02~\mu F$ , and  $R_{EXT}=25~k\Omega$ . Note 5:  $T_A=25^{\circ}C$  and  $V_{CC}=5V$ .

| Switching Characteristics | at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ |
|---------------------------|--|
|---------------------------|--|

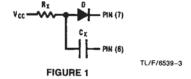
| Symbol              |  | From (Input)<br>To (Output) | DM5   | 64123   | DM74  | Units |    |
|---------------------|--|-----------------------------|---|---|---|-------|----|
|                     | Parameter  |                             | C <sub>L</sub> = 15 pF<br>C <sub>EXT</sub> = 0 pF | , $R_L = 400\Omega$<br>, $R_{EXT} = 5  k\Omega$ | C <sub>L</sub> = 15 pF,<br>C <sub>EXT</sub> = 1000 pF |       |    |
|                     |  |                             | Min   | Max   | Min   | Max   |    |
| t <sub>PLH</sub>    | Propagation Delay Time<br>Low to High Level Output | Ā to Q                      |   | 33  |   | 33    | ns |
| t <sub>PLH</sub>    | Propagation Delay Time<br>Low to High Level Output | B to Q                      |   | 28  |   | 28    | ns |
| <sup>t</sup> PHL    | Propagation Delay Time<br>High to Low Level Output | Ā to Q                      |   | 40  |   | 40    | ns |
| t <sub>PHL</sub>    | Propagation Delay Time<br>High to Low Level Output | B to Q                      |   | 36  |   | 36    | ns |
| t <sub>PLH</sub>    | Propagation Delay Time<br>Low to High Level Output | Clear to Q                  |   | 40  |   | 40    | ns |
| t <sub>PHL</sub>    | Propagation Delay Time<br>High to Low Level Output | Clear to Q                  |   | 27  |   | 27    | ns |
| t <sub>W(out)</sub> | Output Pulse Width*                                | A or B to Q                 | 3.08  | 3.76  | 3.08  | 3.76  | μs |

 $^{\circ}C_{ECT} = 1000 \text{ pF, R}_{EXT} = 10 \text{ k}\Omega$ 

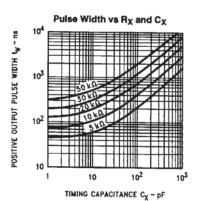
#### **Operating Rules**

- 1. An external resistor (R<sub>X</sub>) and external capacitor (C<sub>X</sub>) are required for proper operation. The value of C<sub>X</sub> may vary from 0 to any necessary value. For small time constants high-grade mica, glass, polypropylene, polycarbonate, or polystyrene material capacitors may be used. For large time constants use tantalum or special aluminum capacitors. If the timing capacitors have leakages approaching 100 nA or if stray capacitance from either terminal to ground is greater than 50 pF the timing equations may not represent the pulse width the device generates.
- When an electrolytic capacitor is used for C<sub>X</sub> a switching diode is often required for standard TTL one-shots to prevent high inverse leakage current (Figure 1). However, its use in general is not recommended with retriggerable operation.
- 3. The output pulse width ( $T_W$ ) for  $C_X > 1000$  pF is defined as follows:

$$\begin{split} T_W &= \text{K R}_X \text{ C}_X \text{ (1 + 0.7/R}_X) \\ \text{where } & [\text{R}_X \text{ is in Kilo-ohm}] \\ & [\text{C}_X \text{ is in pico Farad}] \\ & [\text{T}_W \text{ is in nano second}] \\ & [\text{K} \approx 0.28] \end{split}$$



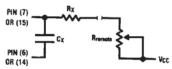
4. For  $C_X <$  1000 pF see Figure 2 for  $T_W$  vs  $C_X$  family curves with  $R_X$  as a parameter:



TL/F/6539-4

FIGURE 2

5. To obtain variable pulse width by remote trimming, the following circuit is recommended:



TL/F/6539-5

Note: "R<sub>remote</sub>" should be as close to the one-shot as possible. FIGURE 3

#### **Operating Rules** (Continued)

6. The retriggerable pulse width is calculated as shown below:

$$T = T_W + t_{PLH} = K \times R_X \times C_X + t_{PLH}$$

The retriggered pulse width is equal to the pulse width plus a delay time period (Figure 4).

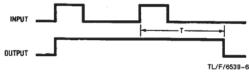
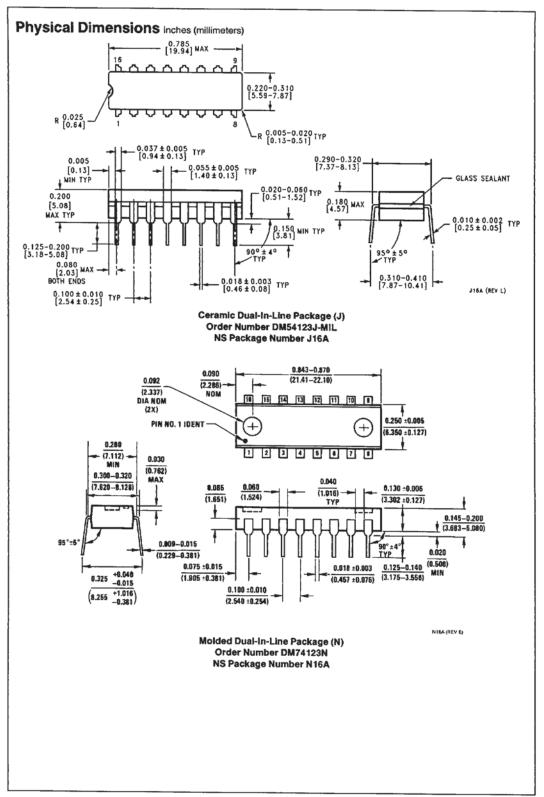


FIGURE 4

- 7. Under any operating condition  $C_X$  and  $R_X$  must be kept as close to the one-shot device pins as possible to minimize stray capacitance, to reduce noise pick-up, and to reduce  $! \times R$  and Ldi/dt voltage developed along their connecting paths. If the lead length from  $C_X$  to pins (6) and (7) or pins (14) and (15) is greater than 3 cm, for example, the output pulse width might be quite different from values predicted from the appropriate equations. A non-inductive and low capacitive path is necessary to ensure complete discharge of  $C_X$  in each cycle of its operation so that the output pulse width will be accurate.
- 8. V<sub>CC</sub> and ground wiring should conform to good high-frequency standards and practices so that switching transients on the V<sub>CC</sub> and ground return leads do not cause interaction between one-shots. A 0.01 μF to 0.10 μF bypass capacitor (disk ceramic or monolithic type) from V<sub>CC</sub> to ground is necessary on each device. Furthermore, the bypass capacitor should be located as close to the V<sub>CC</sub> pin as space permits.

\*For further detailed device characteristics and output performance please refer to the NSC one-shot application note, AN-366.



#### Physical Dimensions inches (millimeters) (Continued) 0.050 - 0.0800.371 - 0.390(1.270 - 2.032)(9.423 - 9.906) $\frac{0.050 \pm 0.005}{(1.270 \pm 0.127)}$ TYP 0.004 - 0.0060.007 - 0.018(0.178 - 0.457)(0.102 - 0.152)TYP -0.000 MIN TYP 0.250 - 0.370(6.350 - 9.398)0.300 16 15 14 13 12 11 10 9 0.245 - 0.275(7.620) (6.223 - 6.985)MAX GLASS 0.008 - 0.012 (0.203 - 0.305)PIN NO. 1 0.250 - 0.370DETAIL A IDENT (6.350 - 9.398)0.026 - 0.0400.015 - 0.019(0.660 - 1.016) TYP-(0.381 - 0.482)W16A (REV H)

16-Lead Ceramic Flat Package (W) Order Number DM54123W-MIL NS Package Number W16A

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