

PART NUMBER 54LS42BEA-ROCV

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

 Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

INCH-POUND

MIL-M-38510/307D

12 March 2003

SUPERSEDING

MIL-M-38510/307C

5 November 1987

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, BIPOLAR LOW-POWER SCHOTTKY TTL, DECODERS, MONOLITHIC SILICON

Inactive for new design after 18 April 1997.

This specification is approved for use by all Departments and Agencies of the Department of Defense.

1. SCOPE

- 1.1 <u>Scope.</u> This specification covers the detail requirements for monolithic silicon, low-power Schottky TTL, decoder microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.3).
 - 1.2 Part number. The part number shall be in accordance with MIL-PRF-38535, and as specified herein.
 - 1.2.1 <u>Device types</u>. The device types shall be as follows:

<u>Device type</u>	<u>Circuit</u>
01	Single 3 to 8 line decoder
02	Dual 2 to 4 line decoder
03	BCD-to-decimal decoder
04	BCD-to-seven segment decoder/driver
	(15-volt, open collector output)

- 1.2.2 <u>Device class</u>. The device class shall be the product assurance level as defined in MIL-PRF-38535.
- 1.2.3 Case outlines. The case outlines shall be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
E	GDIP1-T16 or CDIP2-T16	16	Dual-in-line
F	GDFP2-F16 or CDFP3-F16	16	Flat pack
Χ	CQCC2-N20	20	Square leadless chip carrier
2	CQCC1-N20	20	Square leadless chip carrier

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, P. O. Box 3990, Columbus, OH 43216-5000, by using the self addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

AMSC N/A FSC 5962

1.3 Absolute maximum ratings.

	Supply voltage range	-0.5 V dc to 7.0 V dc
	Input voltage range	-1.5 V dc at -18 mA to 5.5 V dc
	Storage temperature range	-65° to +150°C
	Maximum power dissipation (P _D) 1/	
	Device type 01	55 mW dc
	Device type 02	60.5 mW dc
	Device type 03 and 04	71.5 mW dc
	Output voltage (off state) (device type 04)	15 V dc
	Output current (device type 04)	12 mA dc
	Lead temperature (soldering, 10 seconds)	300°C
•	Thermal resistance, junction to case (θ_{JC}) :	
	Cases E, F, X, and 2	(See MIL-STD-1835)
,	Junction temperature (T _J) <u>2</u> /	175°C
1	Recommended operating conditions.	
	Complex colleges (V/)	1 C \ / ala mainima 1a C C \ / ala

1.4

Supply voltage (V _{CC})	4.5 V dc minimum to 5.5 V dc
	maximum
Minimum high level input voltage (V _{IH})	2.0 V dc
Maximum low level input voltage (V _{IL})	0.7 V dc
Case operating temperature range (T _C)	-55° to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government documents.

2.1.1 Specifications and Standards. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Departments of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

Integrated Circuits (Microcircuits) Manufacturing, General Specification for. MIL-PRF-38535 -

STANDARDS

DEPARTMENT OF DEFENSE

Test Method Standard for Microelectronics. MIL-STD-883

MIL-STD-1835 -Interface Standard Electronic Component Case Outlines

(Unless otherwise indicated, copies of the above specifications and standards are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

^{1/} Must withstand the added P_D due to short-circuit test (e.g., I_{OS}).

^{2/} Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening conditions in accordance with MIL-PRF-38535.

3. REQUIREMENTS

- 3.1 <u>Qualification</u>. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).
- 3.2 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
- 3.3 <u>Design, construction, and physical dimensions.</u> The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.
 - 3.3.1 <u>Terminal connections</u>. The terminal connections shall be as specified on figure 1.
 - 3.3.2 Truth tables. The truth tables shall be as specified on figure 2.
- 3.3.3 <u>Schematic circuits</u>. The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity and the preparing activity upon request.
 - 3.3.4 Case outlines. The case outlines shall be as specified in 1.2.3.
 - 3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).
- 3.5 <u>Electrical performance characteristics</u>. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.
- 3.6 <u>Electrical test requirements</u>. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.
 - 3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.
- 3.8 <u>Microcircuit group assignment.</u> The devices covered by this specification shall be in microcircuit group number 11 (see MIL-PRF-38535, appendix A).

4. VERIFICATION

- 4.1 <u>Sampling and inspection.</u> Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.
- 4.2 <u>Screening.</u> Screening shall be in accordance with MIL-PRF-38535 and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:
 - a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
 - b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
 - c. Additional screening for space level product shall be as specified in MIL-PRF-38535, appendix B.

TABLE I. <u>Electrical performance characteristics</u>.

Test	Symbol	Con	ditions	Device	Lin	nits	Unit
		-55°C ≤ T	_C ≤ +125°C	types	Min	Max	
			wise specified				
High level output voltage	V _{OH1}	$V_{IL} = 0.7 \text{ V}$ $V_{CC} = 4.5 \text{ V}$	I _{OH} = -50 μA	04	2.4		V
High level output voltage	V _{OH2}	V _{IH} = 2.0 V	Ι _{ΟΗ} = -400 μΑ	01, 02, 03	2.5		V
Low level output voltage at BI/RBO	V _{OL1}		I _{OL} = 1.6 mA	04		0.40	V
Low level output voltage	V _{OL2}		I _{OL} = <u>1</u> /	01, 02, 03, 04		0.40	V
Input clamp voltage	V _{IC}		$I_{IN} = -18 \text{ mA},$ $T_{C} = +25^{\circ}\text{C}$	All		-1.5	V
Maximum collector cut-off current	I _{CEX}		V _{OH} = 15 V	04		250	μΑ
Low level input current	I _{IL1}	$V_{CC} = 5.5 \text{ V}$		01	001	38	mA
at A, B, C, D, G		$V_{IN} = 0.4 \text{ V}$		02, 03	03	40	
				04	10	34	
Low level input current at LT and RBI	I _{IL2}			04	11	36	mA
Low level input current at BI/RBO	I _{IL3}			04	36	-1.37	mA
High level input current	I _{IH1}		V _{IN} = 2.7 V	All		20	μΑ
	I _{IH2}		V _{IN} = 7.0 V	All		100	μΑ
Short circuit output	Ios		V _{IH} = 5.5 V <u>2</u> /	01, 03	-6	-130	mA
current	100		V _{IL} = GND	02	-15	-100	
Supply current	I _{CC}		-	01		10	mA
				02		11	
				03, 04		13	
Propagation delay time, low to high level	t _{PLH1}	$V_{CC} = 5.0 \text{ V}$ $C_L = 50 \text{ pF} \pm 10\%$	$R_L = 665\Omega \pm 10\%$	04	5	158	ns
Propagation delay time, low to high level through 2 levels of logic (binary select to output)	t _{PLH2}		$R_L = 2.0 \text{ k}\Omega \pm 10\%$	01, 02	5	38	ns
Propagation delay time, low to high level through 2 levels of logic (A, B, C, or D to output)	t _{PLH2}			03	5	45	ns
Propagation delay time, low to high level	t _{PLH3}			01	5	35	ns
through 2 levels of logic (enable to output)				02	5	44	

See footnotes at end of table.

TABLE I. <u>Electrical performance characteristics</u> - Continued.

Test	Symbol	Cond	ditions	Device	Lir	nits	Unit
		-55°C ≤ T	_C ≤ +125°C	types	Min	Max	
			wise specified				
Propagation delay time,	t _{PLH4}	$V_{CC} = 5.0 \text{ V}$	$R_L = 2.0 \text{ k}\Omega \pm 10\%$	01	5	48	ns
low to high level		$C_L = 50 \text{ pF } \pm 10\%$					
through 3 levels of logic		·		02	5	51	
(binary select to output)							
Propagation delay time,	t _{PLH4}			03	5	53	ns
low to high level							
through 3 levels of logic							
(A, B, C, or D to output)							
Propagation delay time,	t _{PLH5}			01	5	47	ns
low to high level							
through 3 levels of logic							
(enable to output)							
Propagation delay time,	t _{PHL1}		$R_L = 665\Omega \pm 10\%$	04	5	158	ns
high to low level							
Propagation delay time,	t _{PHL2}		$R_L = 2.0 \text{ k}\Omega \pm 10\%$	01	5	69	ns
high to low level							
through 2 levels of logic				02	5	57	
(binary select to output)							
Propagation delay time,	t _{PHL2}			03	5	45	ns
high to low level							
through 2 levels of logic							
(A, B, C, or D to output)							
Propagation delay time,	t _{PHL3}			01, 02	5	56	ns
high to low level							
through 2 levels of logic							
(enable to output)							
Propagation delay time,	t _{PHL4}			01	5	81	ns
high to low level							
through 3 levels of logic				02	5	65	
(binary select to output)							
Propagation delay time,	t _{PHL4}			03	5	53	ns
high to low level							
through 3 levels of logic							
(A, B, C, or D to output)							
Propagation delay time,	t _{PHL5}			01	5	65	ns
high to low level							
through 3 levels of logic							
(enable to output)]			

 $[\]underline{1}/I_{OL} = 4$ mA for device types 01, 02, and 03; $I_{OL} = 12$ mA for device type 04.

 $[\]underline{2}\!/\!\!\!/$ Not more than one output should be shorted at one time.

TABLE II. Electrical test requirements.

	Subgroups	(see table III)
MIL-PRF-38535	Class S	Class B
test requirements	devices	devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 7, 9, 10, 11	1*, 2, 3, 7, 9, 10, 11
Group A test requirements	1, 2, 3, 7, 8, 9, 10, 11	1, 2, 3, 7, 8, 9, 10, 11
Group C end-point electrical parameters	1, 2, 3, 7, 8 9, 10, 11	1, 2, 3
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

^{*}PDA applies to subgroup 1.

- 4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.
- 4.4 <u>Technology Conformance inspection (TCI)</u>. Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).
- 4.4.1 <u>Group A inspection.</u> Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 4, 5, and 6 shall be omitted.
 - 4.4.2 Group B inspection. Group B inspection shall be in accordance with table II MIL-PRF-38535.
- 4.4.3 <u>Group C inspection.</u> Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- 4.4.4 <u>Group D inspection.</u> Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.
 - 4.5 Methods of inspection. Methods of inspection shall be specified and as follows:
- 4.5.1 <u>Voltage and current.</u> All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

	Pin r	number	Pin r	number	Pin r	number	Pin nu	ımber	
	Device	type 01	Device	e type 02	Device	type 03	Device type 04		
				Ca	ises				
Pin number	X, 2	E, F	X, 2	E, F	X, 2	E, F	X, 2	E, F	
1	NC	Α	NC	1G	NC	0	NC	В	
2	Α	В	1G	1A	0	1	В	С	
3	В	С	1A	1B	1	2	С	LT	
4	С	G2A	1B	1Y0	2	3	LT	BI/RBO	
5	G2A	G2B	1Y0	1Y1	3	4	BI/RBO	RBI	
6	NC	G1	NC	1Y2	NC	5	NC	D	
7	G2B	Y7	1Y1	1Y3	4	6	RBI	Α	
8	G1	GND	1Y2	GND	5	GND	D	GND	
9	Y7	Y6	1Y3	2Y3	6	7	Α	е	
10	GND	Y5	GND	2Y2	GND	8	GND	d	
11	NC	Y4	NC	2Y1	NC	9	NC	С	
12	Y6	Y3	2Y3	2Y0	7	D	е	b	
13	Y5	Y2	2Y2	2B	8	С	d	а	
14	Y4	Y1	2Y1	2A	9	В	С	g	
15	Y3	Y0	2Y0	2G	D	Α	b	f	
16	NC	V_{CC}	NC	V_{CC}	NC	V_{CC}	NC	V _{CC}	
17	Y2		2B		С		а		
18	Y1		2A		В		g		
19	Y0		2G		Α		f		
20	V _{CC}		V _{CC}		V _{CC}		V _{CC}		

FIGURE 1. <u>Terminal connections</u>.

DEVICE TYPE 01

		INPUTS	3					OUTI	PUTS			
ENA	ABLE	;	SELECT	-								
G1	G2*	С	В	Α	Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7
Х	Н	Χ	Х	Χ	Н	Н	Н	Н	Н	Н	Н	Н
L	Х	Х	Х	Χ	Н	Н	Н	Н	Н	Н	Н	Н
Н	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н
Н	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н
Н	L	L	Н	Ш	Н	Н	L	Н	Η	Ι	Н	Н
Н	L	L	Н	Ι	Н	Н	Н	L	Η	Ι	Н	Н
Н	L	Н	L	Ш	Н	Н	Н	Н	L	Ι	Н	Н
Н	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н
Н	L	Н	Н	L	Н	Н	Н	Н	Η	Η	L	Н
Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L

*G2 = G2A + G2B

H = high level, L = low level, X = irrelevant.

DEVICE TYPE 02

IN	PUTS		OUTPUTS							
ENABLE	SEL	ECT								
G	В	Α	Y0	Y1	Y2	Y3				
Н	Χ	Χ	Н	Н	Н	Н				
L	L	L	L	Ι	Η	Η				
L	L	Ι	Н	┙	Η	Η				
L	Η	L	Н	Η	L	Н				
L	Η	Τ	Н	Η	Η	L				

H = high level, L = low level, X = irrelevant.

FIGURE 2. Truth tables.

DEVICE TYPE 03

	INP	UTS						OU.	TPUTS				
D	С	В	Α	0	1	2	3	4	5	6	7	8	9
L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	Η	Н	L	Η	Η	Η	Н	Η	Η	Η	Н
L	L	Ι	L	Н	Η	L	Η	Η	Н	Η	Η	Η	Н
L	L	Ι	Η	Н	Η	Η	L	Η	Н	Η	Η	Η	Н
L	Н	L	L	Н	Τ	Τ	Ι	L	Н	Τ	Τ	Ι	Н
L	Н	L	Η	Н	Ι	Ι	Η	Ι	L	Τ	Τ	Ι	Н
L	Н	Ι	L	Н	Ι	Ι	Η	Ι	Н	Ш	Τ	Ι	Н
L	Н	Η	Н	Н	Н	Н	Н	Н	Н	Η	L	Н	Н
Н	L	L	L	Н	Τ	Τ	Ι	Τ	Н	Τ	Ι	Ш	Н
Н	L	Ш	Ι	Н	Τ	Τ	Ι	Τ	Н	Τ	Τ	Ι	L
Н	L	Ι	L	Н	Η	Η	Ι	Η	Н	Η	Η	Η	Н
Н	L	Ι	Н	Н	Н	Н	Н	Н	Н	Η	Н	Η	Н
Н	Н	L	L	Н	Η	Η	Ι	Η	Н	Η	Η	Ι	Н
Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

FIGURE 2. <u>Truth tables</u> - Continued.

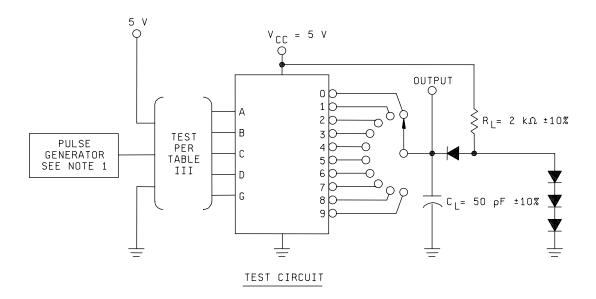
DEVICE TYPE 04

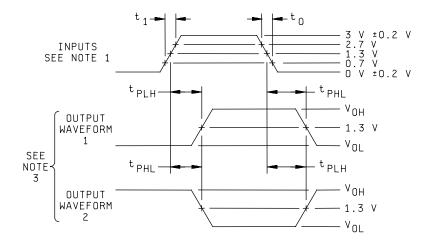
DECIMAL OR	INPUTS							OUTPUTS								
FUNCTION	LT	RBI	D	С	В	Α	BI/RE	30	а	b	С	d	е	f	g	NOTE
0	Н	Н	L	L	L	L	Н		L	L	L	L	L	L	Н	1
1	Н	Х	L	L	L	Н	Н		Н	L	L	Н	Н	Н	Н	1
2	Н	Χ	L	L	Н	L	Н		L	L	Н	L	L	Н	L	
3	Н	Χ	L	L	Н	Н	Н		L	L	L	L	Н	Н	L	
4	Н	Χ	L	Н	L	L	Н		Н	L	L	Н	Н	L	L	
5	Н	Χ	L	Н	L	Н	Н		L	Н	L	L	Н	L	L	
6	Н	Χ	L	Н	Н	L	Н		Н	Н	L	L	L	L	L	
7	Н	Χ	L	Н	Н	Н	Н		L	L	L	Н	Н	Н	Н	
8	Н	Χ	Н	L	L	L	Н		L	L	L	L	L	L	L	
9	Н	Χ	Н	L	L	Η	Н		L	L	L	Н	Н	L	L	
10	Н	Χ	Н	L	Н	L	Н		Н	Η	Н	L	L	Н	L	
11	Η	Χ	Н	L	Н	Η	Н		Н	Η	L	L	Н	Н	L	
12	Η	Χ	Н	Н	L	L	Н		Н	L	Η	Н	Н	L	L	
13	Η	Χ	Н	Н	L	Η	Н		L	Η	Η	L	Н	L	L	
14	Η	Χ	Н	Н	Н	L	Н		Н	Η	Η	L	L	L	L	
15	Н	Χ	Н	Н	Н	Н	Н		Н	Н	Η	Н	Н	Н	Н	
BI	Χ	Χ	Х	Χ	Χ	Х	L		Н	Н	Н	Н	Н	Н	Н	2
RBI	Н	L	L	L	L	L	L		Н	Н	Н	Н	Н	Н	Н	3
LT	L	Χ	Χ	Χ	Χ	Χ	Н		L	L	L	L	L	L	L	4

NOTES:

- 1. BI/RBO is wire-OR logic serving as blanking input (BI) and/or ripple-blanking output (RBO). The blanking input must be open or held at a high logic level when output functions 0 through 15 are desired and ripple-blanking input (RBI) must be held open or at a high logic level during the decimal 0 input, X = input may be high or low.
- 2. When a low logic level is applied to the blanking input (forced condition) all segment outputs go to a high logic level regardless of the state of any other input condition.
- 3. When ripple-blanking input (RBI) is at a low logic level, lamp test input is at high logic level and A = B = C = D = low logic level, all segment outputs go to a high logic level and the ripple-blanking output goes to a low logic level (response condition).
- 4. When blanking input/ripple-blanking output is open or held at a high logic level, and a low logic level is applied to lamp-test input, all segment outputs go to a low logic level.

FIGURE 2. Truth tables - Continued.

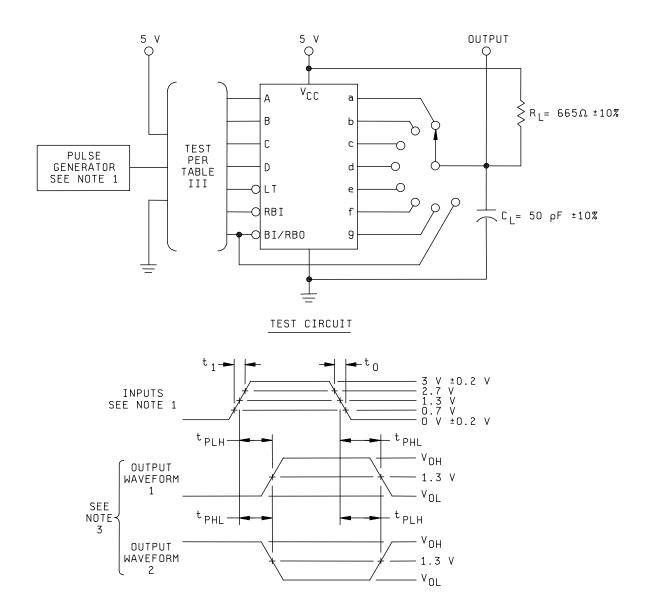




NOTES:

- 1. The pulse generator has the following characteristics: $t_1 \le 15$ ns, $t_0 \le 6$ ns, PRR ≤ 1 MHz.
- 2. C_L includes probe and jig capacitance.
- 3. Input output waveform combination in accordance with the truth tables (see figure 2).
- 4. All diodes are 1N3064, or equivalent.

FIGURE 3. Switching times for device types 01, 02, and 03.



NOTES:

- 1. Pulse generator characteristics: PRR \leq 1.0 MHz, $t_1 \leq$ 15 ns, $t_0 \leq$ 6 ns, and $Z_{out} \approx 50 \Omega$.
- 2. C_L includes probe and jig capacitance.
- 3. Input output waveform combination in accordance with the truth tables (see figure 2).

FIGURE 4. Switching test circuit and waveforms for device type 04.

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases <u>1</u> / 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	iits	Unit
			Test no.	Α	В	С	G2A	G2B	G1	Y7	GND	Y6	Y5	Y4	Y3	Y2	Y1	Y0	V _{CC}		Min	Max	i
1	V _{OH2}	3006	1				OZ/ (025	0.7 V		GND			1				4 mA	4.5 V	Y0	2.5	max	V
Tc = 25°C	* OH2	"	2						"		"						4 mA		"	Y1	"		-
10 - 20 0			3						"							4 mA				Y2			-
			4						"		"				4 mA					Y3			-
			5						"		"			4 mA	.4110				"	Y4			"
			6						"		"		4 mA						"	Y5			"
			7									4 mA							"	Y6			"
			8							4 mA									"	Y7			"
	OL2	3007	9	0.7 V	0.7 V	0.7 V	0.7 V	0.7 V	2.0 V									4 mA	"	Y0		0.4	"
	OLZ	"	10	2.0 V	0.7 V	"	"	"	"		"						4 mA			Y1		"	
			11	0.7 V	2.0 V	"	"	"	"		"					4 mA				Y2			
V			12	2.0 V	2.0 V	"	"	"	"		"				4 mA					Y3			
			13	0.7 V	0.7 V	2.0 V	"	"	"		"			4 mA						Y4			
		"	14	2.0 V	0.7 V	"	"	"	"		"		4 mA							Y5			
			15	0.7 V	2.0 V	"	"	"	"		"	4 mA								Y6			
			16	2.0 V	2.0 V	"	"	"	"	4 mA	"									Y7			
	IC		17	-18 mA							"									Α		-1.5	
			18		-18 mA						"									В			
			19			-18 mA					"									С			
V			20				-18 mA				"									G2A			
			21					-18 mA			"									G2B		"	"
			22						-18 mA		"									G1			
	IL1	3009	23	0.4 V							"								5.5 V	Α	2/	2/	<u>2</u> /
		"	24		0.4 V						"								"	В		"	"
		"	25			0.4 V													"	С		-	"
1		"	26				0.4 V												"	G2A	-	"	"
			27					0.4 V											"	G2B		"	"
		"	28						0.4 V		"								"	G1		"	"
	IH1	3010	29	2.7 V							"									Α		20	μΑ
		"	30		2.7 V						"								"	В			
			31			2.7 V					"								"	С		"	
1		"	32				2.7 V				"									G2A		"	"
		"	33					2.7 V			"									G2B		"	"
		"	34			ļ			2.7 V		"			ļ					- "	G1		"	-
	IH2	3010	35	7.0 V							- "									A		100	-
			36		7.0 V	701				ļ		<u> </u>		<u> </u>		ļ			<u> </u>	В	ļ		-
1			37			7.0 V	7.01/							ļ						C			
1			38			<u> </u>	7.0 V	701/		ļ		<u> </u>		<u> </u>		ļ				G2A	ļ		
			39			ļ		7.0 V	701/					ļ						G2B			
			40		1	1	5.5.1/	5.5.1/	7.0 V			1		1				OND	-	G1	45	400	
	os	3011	41		1	1	5.5 V	5.5 V		 	- "	1		1		 	CND	GND		Y0	-15	-100	mA "
			42			-	"				"			-		GND	GND		-	Y1 Y2			-
1			43 44			-	"	,						-	GND	GND			-	Y2 Y3		,	-
I			44		-	-	"	,			"			GND	GND				"	Y3 Y4		"	-
			45		1	}				 		1	GND	GND	 	 	 		"	Y4 Y5		"	-
			47		1	1						GND	GIND	1					"	Y6		"	"
			48		1	1				GND		GIND		1					"	Y7		"	"
	CC	3005	49		<u> </u>	 	GND	GND	5.5 V	GIND				 					"	V _{CC}		10.0	
2			al conditions	and limita	a ac cuhar	oun 1 ove				omitted	1			l	l	l	l		l	V CC	l	10.0	
3			al conditions																				
<u> </u>	Janie le	ooto, terrillille	ai conunions	and mills	as subgit	oup i, exc	opt IC = -0	o Canu V	IC ICOIO UII	inticu.													

See footnotes at end of device type 01.

TABLE III. Group A inspection for device type 01 - Continued. 1/

			Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
		MIL-STD-	E, F		_	-	-								.=								
Subgroup	Symbol	883	Cases 1/	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured	Lim	nits	Unit
	1	method	2, X																	terminal			
			Test no.	Α	В	С	G2A	G2B	G1	Y7	GND	Y6	Y5	Y4	Y3	Y2	Y1	Y0	V _{cc}		Min	Max	
7 <u>3</u> /	Truth	3014	50	Α	Α	Α	Α	Α	Α	Н	GND	Н	Н	Н	Н	Н	Н	Н	5.0 V				
Tc = 25°C		"	51	В	Α	"	"	"	"	"	"	"	"	"			"		"				
	tests		52	"	В	- 1		"		"		-	"	"		"			"				
		i i	53	- "		B				- ;				"	- ;	- :		-			<u>3</u> /		
			54 55	"	"	"	B A	A B		"	"		-	"									
			56				A	В	В					"			"		"				
		"	57	"	"		В	A	"				"	"			"	"	"				
		"	58		"			В	"					"			"		"				
		"	59	-	"	"	"	"	Α	-	-		"	"	-	-	"	L	"				
		"	60	Α			"			-			"	"			L	Н	"				
			61	В	Α	"		"				"		"		L	Н		"				
			62	A	A	"		"		-					L	Н			- :				
		,	63 64	B A	B B	Α "	"	"	"	,	"			H	H								
			65	В	A	"	"	"	"	"		L	H				"						
		"	66	A	A	"	"	"	"	L	"	Н	H	"			"						
8 <u>3</u> /	Truth	3014	67																				
_	table		thru	Same tes	sts as subg	group 7, ex	cept T _C =	+125°C															
			83																				
		3014	84	_			_	_															
test	table		thru 100	Same tes	sts as subg	group 7, ex	cept T _C =	-55°C															
9Truth	t	3003	101 & 102	IN	GND	GND	GND	GND	5.0 V		GND			l				OUT	5.0 V	A to Y0	5	25/46	ns
Tc = 25°C	t _{PLH2} t _{PHL2}	Fig. 3	103 & 104	"	5.0 V	GND	"	UND "	3.0 V		UND "					OUT		001	3.0 V	A to Y2	"	23/40	"
test	THILZ	"	105 & 106	"	GND	5.0 V	"	"	"		"			OUT						A to Y4		"	
		"	107 & 108	"	5.0 V	5.0 V	"	"	"		"	OUT							"	A to Y6		"	"
		"	109 & 110		IN	GND		"	"									OUT		B to Y0		"	
		"	111 & 112	5.0 V	"	GND	"	"	"								OUT			B to Y1		"	"
			113 & 114	GND	"	5.0 V		- "	-		-		OUT	OUT					"	B to Y4		- "	-:-
			115 & 116 117 & 118	5.0 V GND	GND	5.0 V IN					-		OUT					OUT	- "	B to Y5 C to Y0		"	- "
			119 & 120	5.0 V	GND	IIN "											OUT	001	"	C to Y1		"	"
			121 & 122	GND	5.0 V											OUT	001		"	C to Y2		"	"
		"	123 & 124	5.0 V	5.0 V			"							OUT				"	C to Y3		"	"
	PLH3		125 &126	GND	GND	GND	IN	"	"									OUT	"	G2A to Y0	"	23/37	"
	PHL3	Fig. 3	127 & 128	5.0 V	GND	"	"	"	"		"						OUT			G2A to Y1			
	30	03 "	129 & 130	GND	5.0 V	"		"			"				OUT	OUT				G2A to Y2	- :	"	
t t			131 & 132	5.0 V	5.0 V				- "					OUT	OUT					G2A to Y3			-
			133 & 134 135 & 136	GND 5.0 V	GND GND	5.0 V	"	"	"				OUT	OUT		1				G2A to Y4 G2A to Y5			
			137 & 138	GND	5.0 V	"	"	"	"		"	OUT	001							G2A to Y6			
		"	139 & 140		5.0 V	"	"	"	"	OUT	"	551								G2A to Y7			
		"	141 & 142	GND	GND	GND	GND	IN	"					1				OUT	"	G2B to Y0		"	
		"	143 & 144	5.0 V	GND	"	"	"	"		"						OUT		"	G2B to Y1		"	"
		"	145 & 146	GND	5.0 V	"	"	"	"		"					OUT			-	G2B to Y2	"	"	"
			147 & 148	5.0 V	5.0 V	"			"						OUT				- "	G2B to Y3		"	
			149 & 150	GND	GND	5.0 V			"				OUT	OUT					-:-	G2B to Y4		- "	-
			151 & 152 153 & 154	5.0 V GND	GND 5.0 V	"		"				OUT	OUT						-	G2B to Y5 G2B to Y6	-	-	-
			153 & 154	5.0 V	5.0 V	"	-	-		OUT		001				-				G2B to Y6	-		
	ı		וטט ע וטט	J.U V	J.U V			L		OUI				L		L	ı		1	32D (0 17			

See footnotes at end of device types 01.

TABLE III. Group A inspection for device type 01 - Continued. 1/

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases <u>1</u> / 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lin	nits	Unit
			Test no.	Α	В	С	G2A	G2B	G1	Y7	GND	Y6	Y5	Y4	Y3	Y2	Y1	Y0	V_{CC}		Min	Max	
9	t _{PLH4}		157 &158	IN	GND	GND	GND	GND	5.0 V		GND						OUT		5.0 V	A to Y1	5	32/54	ns
Tc = 25°C	t _{PHL4}	Fig. 3	159 & 160	"	5.0 V	GND	=	"	"		"				OUT				"	A to Y3		"	
	30	03 "	161 & 162	"	GND	5.0 V		"	"		"		OUT							A to Y5			
		"	163 & 164	"	5.0 V	5.0 V	"	"	"	OUT	"									A to Y7			
		"	165 & 166	GND	IN	GND	"	"	"		"					OUT			"	B to Y2		"	
		"	167 & 168	5.0 V	"	GND	-	"							OUT				"	B to Y3		"	"
		"	169 & 170	GND	"	5.0 V						OUT							"	B to Y6		"	"
		"	171 & 172	5.0 V	"	5.0 V				OUT									"	B to Y7		"	"
		"	173 & 174	GND	GND	IN								OUT					"	C to Y4		"	"
		"	175 & 176	5.0 V	GND				-		-		OUT						"	C to Y5	-	"	-
		"	177 & 178	GND	5.0 V				-			OUT							"	C to Y6	-	"	=
		"	179 & 180	5.0 V	5.0 V		-	"	=	OUT	-								"	C to Y7	-	"	-
	PLH5	3003	181 & 182	GND	GND	GND	-	"	IN		-							OUT	"	G1 to Y0	=	31/43	-
	PHL5	Fig. 3	183 & 184	5.0 V	GND		"	"	=		"						OUT		"	G1 to Y1		"	
		"	185 & 186	GND	5.0 V	"	"	"	"		"					OUT			"	G1 to Y2		"	
t		"	187 & 188	5.0 V	5.0 V	"	-	"	=		=				OUT				"	G1 to Y3	-	"	-
t		"	189 & 190	GND	GND	5.0 V	-	"	=		"			OUT					"	G1 to Y4		"	
		"	191 & 192		GND	"	"	"	"		"		OUT						"	G1 to Y5		"	"
		"	193 & 194		5.0 V		=	"	=			OUT							"	G1 to Y6		"	-
		"	195 & 196	5.0 V	5.0 V	"		"	"	OUT	"								"	G1 to Y7		"	

Same tests and terminal conditions as for subgroup 9, except $T_C = +125^{\circ}C$ and for following limits: $t_{PLH2} = 5$ to 38 ns; $t_{PHL2} = 5$ to 69 ns; $t_{PLH3} = 5$ to 35 ns; $t_{PHL3} = 5$ to 56 ns; $t_{PLH4} = 5$ to 48 ns; $t_{PHL4} = 5$ to 81 ns; $t_{PHL5} = 5$ to 47 ns; $t_{PHL5} = 5$ to 65 ns.

Same tests, terminal conditions and limits as for subgroup 10, except $T_C = -55^{\circ}C$.

		Min/Ma	x limits (mA) for	circuits	
I _{IL1}	Α	В	C, E, F	D	G
	001/150 except 11/35 for tests 26, 27, 28	030/300	12/36	10/34	15/38

^{3/} A = 2.5 V and B = 0.4 V; H \geq 1.5 V; L \leq 1.5 V.

^{1/} Pins not designated are high \geq 2.0 V; low \leq 0.7 V; or open. Case X and 2 pins not referenced are NC. 2/ I_{IL1} limits are as follows:

TABLE III. Group A inspection for device type 02. 1/

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883	Cases 1/	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured	Lim	its	Unit
		method	2, X																	terminal			
			Test no.	1G	1A	1B	1Y0	1Y1	1Y2	1Y3	GND	2Y3	2Y2	2Y1	2Y0	2B	2A	2G	V _{CC}		Min	Max	
1	V_{OH2}	3006	1	2.0 V						4 mA	GND								4.5 V	1Y3	2.5		V
$Tc = 25^{\circ}C$		"	2	"					4 mA		"									1Y2			"
		"	3					4 mA			"									1Y1			
		"	4	"			4 mA				"								"	1Y0	"		"
			5								"	4 mA						2.0 V	"	2Y3			
			6										4 mA						"	2Y2	- :		
			7								-			4 mA					"	2Y1	- :		
			8	071/	0.01/	0.01/					- "				4 mA				- "	2Y0		0.4	
	OL2	3007	9	0.7 V	2.0 V	2.0 V				4 mA										1Y3		0.4	
			10	"	0.7 V	2.0 V		4 4	4 mA		-								- "	1Y2		- "	
V			11	-	2.0 V	0.7 V	4 mm A	4 mA												1Y1			
·			12		0.7 V	0.7 V	4 mA					1 mm A				201/	201/	0.7.1/		1Y0 2Y3			
			13 14									4 mA	4 mA			2.0 V 2.0 V	2.0 V 0.7 V	0.7 V		2Y2			
		,,	15										4 MA	4 mA		0.7 V	2.0 V			2Y2 2Y1			-
		,,	16											4 IIIA	4 mA	0.7 V	0.7 V			2Y0			
	10		17	-18 mA							"				4111/	0.7 V	0.7 V			1G		-1.5	
	IC		18	-10 111/4	-18 mA															1A		"	
			19		-10 111/4	-18 mA														1B			
V			20			-10 111/4					"					-18 mA				2B		"	"
·			21													1011111	-18 mA		"	2A		"	"
			22														101117	-18 mA	"	2G		"	
	IL1	3009	23	0.4 V							"								5.5 V	1G	2/	2/	mA
	IL1	"	24		0.4 V						"								"	1A	7	-	"
			25			0.4 V					"								"	1B			
1		"	26								"					0.4 V			"	2B		"	-
,		"	27								"						0.4 V		"	2A		"	-
		"	28								"							0.4 V	"	2G		"	"
	IH1	3010	29	2.7 V							"									1G		20	μΑ
		"	30		2.7 V						"								"	1A		"	. "
		"	31			2.7 V					"								"	1B		"	-
1		"	32								"					2.7 V			"	2B			"
		"	33								"						2.7 V		"	2A			
			34								=							2.7 V	-	2G		-	-
	IH2	3010	35	7.0 V							"									1G		100	
		"	36		7.0 V						"								"	1A		"	"
		"	37			7.0 V					"					ļ			"	1B		"	
		"	38								- "					7.0 V				2B		"	
			39														7.0 V			2A			
			40								-					ļ		7.0 V	"	2G		"	
	os	3011	41	5.5 V			GND				-					ļ				1Y0	-15	-100	mA "
			42	"				GND	OND			1				ļ				1Y1			
1			43	- ;					GND	OND	- "	1				ļ				1Y2			
			44		1	1				GND	- "	CND		-		1		EEV	- "	1Y3	-		
			45		1	1						GND	CND	-		1		5.5 V	- "	2Y3 2Y2	- 1		-
		,,	46								"		GND	CND		-							
		,	47											GND	CND	-				2Y1 2Y0			
		3005	48 49	GND	5.5 V	5.5 V					"				GND	E E \/	5.5 V	GND				11.0	-
2	CC Come to						ont T - ·	125°C cod	V toots	mittad		1		L		5.5 V	υ.ט V	טאט	l	V _{CC}		11.0	
			al conditions																				
3	same te	ະຣເຣ, ເermin	al conditions	anu iimits	s as subgr	oup i, exc	e pւ r _C = -5	ນວ⁻ບ and \	IC LESTS ON	iiilea.													

See footnotes at end of device type 02.

TABLE III. Group A inspection for device type 02 - Continued. 1/

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
L			,																				
Subgroup	Symbol	883 method	Cases <u>1</u> / 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	1G	1A	1B	1Y0	1Y1	1Y2	1Y3	GND	2Y3	3Y2	2Y1	2Y0	2B	2A	2G	V_{CC}		Min	Max	
7 <u>3</u> /	Truth	3014	50	Α	Α	Α	Н	Н	Н	Н	GND	Н	Н	Н	Н	Α	Α	Α	5.0 V				
Tc = 25°C	table		51	"	В	Α	"	"	"	"	"			"	"	Α	В		"				
	tests		52		Α	В								"		В	Α		"				
		"	53	"	В	"	"		"	"	"		"	"	"		В				<u>3</u> /		
		"	54	В	В	"	L	"	"	"	"			"	L	"	В	В	"		_		
		"	55	"	Α	"	Н	L	"	"	"			L	Н	"	Α		"				
			56	"	В	Α	"	Н	L	"	"		L	Н	"	Α	В		"				
		"	57	"	Α	Α	"	Н	Н	L		L	Н	Н	"	Α	Α		"				
8 <u>3</u> /	Truth	3014	58																				
	table		thru 65	Same tes	sts as sub(group 7, ex	cept T _C =	+125°C															
		3014	66																				
	table	3014	thru	Same too	sts as subo	aroun 7 ev	cent T	-55°€															
test	table		73	Oame tes	วเจ ผจ จนม(group 1, ex	copt 16 -	-55 0															
9 ^{Truth}	t _{PLH3}	3003	74 & 75	IN	GND	GND	OUT				GND								5.0 V	1G to 1Y0	5	29/37	ns
Tc = 25°C	t _{PHL3}	Fig. 3	76 & 77	"	5.0 V	GND	001	OUT			"								U.U V	1G to 1Y1	"	"	"
test	PHL3	g. o	78 & 79		GND	5.0 V			OUT		"									1G to 1Y2			
1001			80 & 81	"	5.0 V	5.0 V				OUT	"									1G to 1Y3			-
	PLH2	"	82 & 83	GND	IN	GND	OUT				"								"	1A to 1Y0		25/38	-
	PHL2		84 & 85	"	IN	5.0 V			OUT		"									1A to 1Y2		"	
			86 & 87	"	GND	IN	OUT				"								"	1B to 1Y0		"	
t			88 & 89	"	5.0 V	IN		OUT			"								"	1B to 1Y1		"	
t	PLH3	"	90 & 91								"				OUT	GND	GND	IN		2G to 2Y0		29/37	"
	PHL3		92 & 93								"			OUT		GND	5.0 V		"	2G to 2Y1		"	"
		"	94 & 95								-		OUT			5.0 V	GND			2G to 2Y2			-
t		"	96 & 97								=	OUT				5.0 V	5.0 V		"	2G to 2Y3		"	"
t	PLH3	"	98 & 99								"				OUT	GND	IN	GND	"	2A to 2Y0		25/38	
	PHL3		100 & 101								"		OUT			5.0 V	IN	"	"	2A to 2Y2		"	
			102 & 103								"				OUT	IN	GND		"	2B to 2Y0		"	
t			104 & 105								"			OUT		IN	5.0 V	"	"	2B to 2Y1		"	
τ	PLH4		106 & 107	GND	IN	GND		OUT			"								"	1A to 1Y1		34/43	
	PHL4		108 & 109	- "	IN	5.0 V				OUT										1A to 1Y3		- "	
1 .			110 & 111		GND	IN			OUT		- "					ļ			- "	1B to 1Y2		"	
t .			112 & 113	"	5.0 V	IN				OUT	"								"	1B to 1Y3		"	
t			114 & 115		ļ									OUT		GND	IN	GND		2A to 2Y1			
			116 & 117								- "	OUT				5.0 V	IN		- "	2A to 2Y3		"	
			118 & 119								<u>"</u>		OUT			IN	GND		<u>"</u>	2B to 2Y2			
10			120 & 121		L.	l			L			OUT		l	L	IN	5.0 V			2B to 2Y3	-		
		ests and terr							rtollowing	IImits: t _{PLH}	$_{12} = 5 \text{ to } 38$	ns; t _{PHL2}	= 5 to 57 ns	$S; t_{PLH3} = 5$	to 44 ns; t _P	$_{HL3} = 5 \text{ to } 5$	6 ns; t _{PLH4}	= 5 to 51 n	$s; t_{PHL4} = $	5 to 65 ns.			
11	Same t	ests, termina	al conditions	and limits	s as for sub	ogroup 10,	except T _C	: = -55°C															

 $[\]underline{1}/$ Pins not designated are high \geq 2.0 V; low \leq 0.7 V; or open. Case X and 2 pins not referenced are NC. $\underline{2}/$ I_{IL1} limits are as follows:

		Min/Max limits	(mA) for circuit	S
I _{IL1}	A, C, E, F	В	D	G
	012/36	03/300	10/34	15/38

 $[\]underline{3}/\ H \ge 1.5\ V;\ L \le 1.5\ V;\ A\ = 2.5\ V;\ B = 0.4\ V.$

TABLE III. Group A inspection for device type 03. 1/

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
ubgroup	Symbol	883 method	Cases <u>1</u> / 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}		Min	Max	1
1	V _{OH2}	3006	1	4 mA							GND				2.0 V	2.0 V	2.0 V	2.0 V	4.5 V	0	2.5		V
c = 25°C	OHZ	"	2		mA															1			
		"	3			4 mA					"							"	"	2			"
		"	4				4 mA											"	"	3			"
		"	5	4				4 mA								"		"	"	4			"
		"	6						4 mA							"		"	"	5	"		"
		"	7							4 mA								"		6			
		"	8								"	4 mA				"				7			
		"	9								"		4 mA			"		"		8			-
		"	10											4 mA		"		"	"	9			"
	OL2	3007	11	4 mA							"				0.7 V	0.7 V	0.7 V	0.7 V	"	0		0.4	"
	OLZ	"	12		4 mA										"	"	0.7 V	2.0 V	"	1		"	
		"	13			4 mA					"					"	2.0 V	0.7 V		2			-
V		"	14				4 mA				"						2.0 V	2.0 V		3			
		"	15					4 mA			"				"	2.0 V	0.7 V	0.7 V		4		"	-
		"	16						4 mA		"					"	0.7 V	2.0 V		5		"	
		"	17							4 mA	"					"	2.0 V	0.7 V		6		"	
		"	18								"	4 mA				"	2.0 V	2.0 V		7		"	
		"	19								"		4 mA		2.0 V	0.7 V	0.7 V	0.7 V	"	8		"	"
		"	20								"			4 mA	2.0 V	0.7 V	0.7 V	2.0 V	"	9		"	-
F	IC		21								"					***		-18 mA		Ā		-1.5	
	10		22								"						-18 mA			В		"	
			23													-18 mA				C			
V			24								"				-18 mA					D			
	IH1	3010	25								"							2.7 V	5.5 V	Α		20	μА
	III I	"	26								"						2.7 V		"	В		"	"
		"	27								"					2.7 V			"	C		"	"
1		"	28												2.7 V				"	D		"	"
·	IH2	3010	29								"							7.0 V		A		100	
	IIIZ	"	30								"						7.0 V			В		"	
		"	31								"					7.0 V	7.0 7		"	C		"	"
1		"	32								"				7.0 V	7.0 1			"	D		"	"
·	_{IL1} 2/	3009	33								"							0.4 V		A	2/	2/	mA
	IL1 =	"	34								"						0.4 V	0	"	В	=	-	"
		"	35								"					0.4 V	U		"	C		"	"
1		"	36								"				0.4 V					D			-
ļ-	os 3_/	3011	37	GND							"				5.5 V	5.5 V	5.5 V	5.5 V	"	0	-15	-100	"
	J3 3 ⊥⁄	"	38	0.10	GND						"				"	"	"	"		1	"	"	"
		"	39			GND										"		"	"	2		"	"
ı		"	40				GND								"	"		"	"	3		"	"
		"	41					GND								"	"		"	4		"	"
		"	42						GND						"	"	"	"	"	5	"	"	"
		"	43						0	GND						"		"	"	6	"	"	"
		"	44									GND				"	"	"	"	7	"	"	"
		"	45								"		GND		"	"	"	"	"	8	"	"	-
		"	46										0.10	GND	"	"	"	"	"	9	"	"	-
		3005	47				1				"				GND	GND	GND	GND	"	V _{CC}		13	
J	CC																						

See footnotes at end of device type 03.

TABLE III. Group A inspection for device type 03 - Continued. 1/

			0					-	_		_		40	- 44	40	40	4.4	45	1 40 1				
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol		Cases <u>1</u> /	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured	Lin	nits	Unit
		method	2, X																	terminal			
			Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	A	V _{CC}		Min	Max	
7 <u>4</u> /	Truth	3014	48	L	Н	Н	Н	Н	Н	Н	GND	Н	H	Н	В	В	В	В	5.0 V				
Tc = 25°C	table		49	Н	L	Н						- "					В	A					
	tests		50		Н	L		- :				- "		- "			A	В					
			51			H	L.		- :		- "	- "		- "			A	A			<u>4</u> /		
			52	- "		- "	H	L			- "	- "				A	В	В					
			53 54	"				H	H		"			-			В	A B	-				
				-	-		-		H	<u>L</u>			-	"	-		A		-				
			55 56		-		-		-	<u>H</u>	-	H		"		В	A B	A B	- "				
			57							-		п п	H	L	A		В	A	-				
			58											Н			A	В	"				
		"	59											"			A	A	"				
			60	"	"	"	"	"	"	"	"		"	"		Α	В	В					
		"	61	"	"	"	"	"	"	"	"			"			В	A	-				
			62	"	"	"		"	"	"				"			A	В					
			63	"	"	"	"	"	"	"	"			"			A	A					
8 4/	Truth	3014	64		l .	1										1			1				
<u> </u>	table	00	thru	Same tes	sts as subc	group 7, ex	cent Tc =	+125°C															
			79			J																	
		3014	80																				
	table			Same tes	sts as subc	group 7, ex	cept T _C =	-55°C															
test.			95																				
₉ Truth	t _{PLH4}	3003	96 & 97			OUT					GND				GND	GND	IN	GND	5.0 V	B to 2	5	35	ns
Tc = 25°C	t _{PHL4}	Fig. 3	98 & 99		OUT						"					GND	GND	IN		A to 1		"	
test			100 & 101								=	OUT			"	5.0 V	5.0 V	IN	"	A to 7	-	"	"
	PLH2		102 &103		OUT						=				"	GND	IN	5.0 V		B to 1		30	"
	PHL2	"	104 & 105					OUT			=				"	5.0 V	IN	GND		B to 4		"	"
			106 & 107	OUT							"				"	GND	GND	IN	"	A to 0		"	"
t t			108 & 109			OUT					"				"	IN	5.0 V	GND	"	C to 2	"	"	"
l l		"	110 & 111				OUT				"				"	"	5.0 V	5.0 V	"	C to 3	-	"	"
			112 & 113								"		OUT		5.0 V	"	GND	GND	"	C to 8		"	
		"	114 & 115								"			OUT	5.0 V			5.0 V	"	C to 9		"	
	PLH4		116 & 117					OUT							GND	- "		GND		C to 4		35	
	PHL4		118 & 119						OUT	0.17	- "					- "	501/	5.0 V		C to 5		- "	
			120 & 121							OUT		OUT.					5.0 V	GND		C to 6	-		
ι +			122 & 123				OUT					OUT				OND.		5.0 V	L	C to 7		"	- :
,	PLH2		124 & 125				OUT	OUT			- "				IN	GND	OND	5.0 V		D to 3	-	30	
	PHL2	i i	126 & 127					OUT	OUT							5.0 V	GND	GND		D to 4	-	-	
t		,	128 & 129						OUT	OUT	- "					-	GND	5.0 V		D to 5	-	-	
t		,	130 & 131							OUT	"	OUT				- "	5.0 V	GND		D to 6	-		
,			132 & 133								"	OUT	OUT		-		5.0 V	5.0 V		D to 7	-	25	-
	PLH4	,	134 & 135 136 & 137										OUT	OUT	-	GND	GND GND	GND 5.0 V	- "	D to 8		35	-
10	PHL4			ione ee fer	oubaro	0 0000=+	T - 1125	°C and far	followina	limito: t	- E to 45	no: t	_ E to 1E ~~		to E2 no: 1	GND - 5 to 5		5.U V	1	D to 9	1	35	
10 1፟	Same to	esis and teri	minal conditi	ons as for	subgroup	9, except	1 _C = +125	-c and for	iollowing	IIIIITS: T _{PLH}	$_2 = 5 \text{ to } 45$	ris; t _{PHL2}	= 5 to 45 ns	s ; $\iota_{PLH4} = 5$	เบ อง ทร; t _{Pl}	$_{HL4} = 5 10 5$	os ns.						
		esis termina	al conditions	and limits	as for sub	ogroup 10,	except In	= -55°C															

^{1/} Pins not designated are high \geq 2.0 V; low \leq 0.7 V; or open. Case X and 2 pins not referenced are NC. 2/ I_{IL1} limits are as follows:

					Min/Max limits	s (mA) for circuit	S
I _{IL1}	Α	В	С	D	E	F	G
	12/36	030/300	12/36	12/36	12/36	12/36	16/40

 $[\]underline{3}/$ For circuit C, limits are: -15 to -130 mA. $\underline{4}/$ H \geq 1.5 V; L \leq 1.5 V; $_{A}$ = 2.5 V; B = 0.4 V.

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases <u>1</u> / 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	its	Unit
			Test no.	В	С	LT	RBO	RBI	D	Α	GND	е	d	С	b	а	g	f	V_{cc}		Min	Max	İ
1	V_{OL2}	3006	1	Χ	Х	0.7 V		Х	Χ	X	GND	12 mA							4.5 V	е		0.4	V
Tc = 25°C		"	2	"	"	"		"		"	"		12 mA							d			"
		"	3	"	"	"		"	"	"	"			12 mA						С			"
		"	4	"	"	"		"	"	"	"				12 mA					b			"
		"	5	"	"	"		"	"	"	"					12 mA				а			"
		"	6	"	"	"		=	"	"	-						12 mA			g			
		"	7	"	"	"		=	=		-							12 mA		f			-
	OL1	3006	8	0.7 V	0.7 V	2.0 V	1.6 mA	0.7 V	0.7 V	0.7 V									"	RBO		"	"
	CEX		9	"	"	"		=	"	=	=	15 V							"	е		250	μΑ
			10	"	"	"		=	"	"	-		15 V							d			
V			11	"	"	"		"	"	"	"			15 V						С			
I			12	"	"	"		"	"	"	"				15 V					b			
			13	"	"	"		"	"	"	"					15 V			"	а			
			14	"	"	"		"	"	"	"						15 V		"	g			-
			15	"	"	"		"	"	"	"							15 V	"	f			-
	OH1	3007	16	0.7 V	0.7 V	2.0 V	-50 μΑ	2.0 V	0.7 V	0.7 V	"								"	RBO	2.4		V
	IC		17	-18 mA							"								"	В		-1.5	
V			18		-18 mA						"								"	С			
V			19			-18 mA					"								"	LT			
V			20					-18 mA			"								"	RBI		"	
			21						-18 mA											D			
			22							-18 mA	"									A			
	IL1	3009	23	0.4 V	5.5 V	5.5 V		5.5 V	5.5 V	5.5 V	"								5.5 V	В	10	34	mA
		"	24	5.5 V	0.4 V	"			5.5 V	"	- "								- "	C			<u> </u>
			25		5.5 V	"			0.4 V											D			
1			26						5.5 V	0.4 V										A	- 0/		
	IL2	3009	27 28	- "		0.4 V 5.5 V		0.4 V		5.5 V					-				-	LT RBI	<u>2</u> /	<u>2</u> /	-
		3009	29	-		5.5 V	0.4 V	5.5 V							-				,	RBO			-
	IL3	3010	30	2.7 V	GND	GND	0.4 V	GND	GND	GND					-				,	В		20	_
'	IH1	3010	31	GND	2.7 V	GND		GND	GND	GND					-				,	С		20	μA "
		,	32	GND	GND	2.7 V			-										,	LT		-	—
			33	-	GND	GND		2.7 V												RBI			-
'		,,	34	-	"	GIND		GND	2.7 V										"	D		"	
		,,	35	-	"			GIND	GND	2.7 V									"	A		"	
		3010	36	7.0 V	"	"		"	UND "	GND	"					 				B		100	-
	IH2	"	37	GND	7.0 V	"	 	"	"	"	"			 		 			-	C		"	-
		"	38	"	GND	7.0 V	1									1				LT			
		"	39	"	"	GND	1	7.0 V	"	"	"					1				RBI			
'		"	40	"	"	"	1	GND	7.0 V	"	"					1				D			
		"	41	"	"	"	1	GND	GND	7.0 V						1			"	A			
	CC	3005	42	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V						 			"	V _{CC}		13	mA
2			al conditions										l	1	1		1		1	• • • • • • • • • • • • • • • • • • • •	l		
			al conditions																				
	Jame	ooto, terrillii	ai conditions	and iiiillo	, as subgit	Jup 1, GAU	υρι 10 – - 0	o o and v	IC ICOIO UII	muou.													

See footnotes at end of device type 04.

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							7	TABLE I	II. Grou	ın A ins	pection	for dev	ice type	04 - Cor	ntinued.	1/								
		MIL-STD-	Cases E. F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16					
Subgroup	Symbol		Cases <u>1</u> / 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	its	Unit	
			Test no.	В	С	LT	RBO	RBI	D	Α	GND	е	d	С	b	а	g	f	V _{cc}		Min	Max		
7	Truth	3014	43	В	В	Α	Α	Α	В	В	GND	L	Ĺ	L	Ĺ	L	H	L	5.0 V			1		
Tc = 25°C	table	"	44	В	"	"	-	Х	"	Α	"	Н	Н	L	-	Н	Н	Н						
<u>3</u> /, <u>4</u> /	tests	"	45	Α				"	"	В	"	L	L	Н		L	L	-	"					
		"	46	Α	"	"	"	"	"	Α	"	Н	L	L		L					<u>5</u> /			
			47	В	A	"		- "	- "	В	"		<u>H</u>	"	- :	H	- "	Ŀ	- "					
			48 49	B A	"	"	"	"	"	A B	"	L	<u>L</u>	"	H	L H			"					
		"	50	A	"	"	"	"	"	A	"	Н	H	"		L	Н	Н	"					
		"	51	В	В				Α	В		ï	ï	"	-	-	ï	ï	"					
		"	52	В	"	"		"		Ā	"	H	H	"			-	Ē	"					
		"	53	Α	"	"	"	"	"	В	"	L	L	Н	Н	Н	"	Н	"					
		"	54	Α	"	"	"	"	"	Α	"	Н	L	L	Н		"	Н	"					
		"	55	В	Α	"	"	"	"	В	"		Н	Н	L		"	L	"					
			56	В	"	"		"	- "	A	"		<u> </u>	"	Н	L	-	- "	"					
			57	A	- "	- "	- "	- "	- "	В	"	L	<u> </u>	"	- :	H		- :-	- "					
		,	58 59	A X	X	X	В		X	A X		H	<u>H</u>	"			H	H	"					
		"	60	B	B	5.0 V	L	В	B	B									"					
		"	61	X	X	B B	A	X	X	X	"	L	L	L		L	L	L	"					
8	Truth	3014	62																					
<u>3</u> /, <u>4</u> /	table		thru	Same tes	sts as subo	group 7, ex	cept T _C =	+125°C																
			80																					
		3014	81																					
	table		thru	Same tes	sts as subo	group 7, ex	cept T _C =	-55°C																
test 9Truth	4	3003	99 100 & 101	GND	GND	F O V		501/	CND	INI	GND			1		OUT	1		E 0 \/	A to a	-	105		
Tc = 25°C	t _{PLH1}	Fig. 4	100 & 101	GND	5.0 V	5.0 V		5.0 V	GND 5.0 V	IN "	GIND "					001			5.0 V	A to a	5	105	ns "	
test	t _{PHL1}	1 ig. 4	104 & 105	5.0 V	3.0 V	"		"	GND	"	"									A to a			-	
		"	106 & 107	GND	"	"		"	"	"	"				OUT					A to b				
		"	108 & 109	5.0 V	"	"		"	"	"	"				OUT					A to b				
		"	110 & 111	5.0 V	GND	"		"	"	"	"			OUT					"	A to c		"	"	
		"	112 & 113	GND	GND	"		"	"	"	"		OUT						"	A to d	-	"	"	
		"	114 & 115		5.0 V	"		"	"	"	"		"						"	A to d		"	"	
		"	116 & 117	5.0 V	5.0 V	"		"	"	"	"		"							A to d		"		
			118 & 119		GND "	"		"	"	- "	"	OUT				ļ				A to e	-			
			120 & 121 122 & 123	5.0 V GND	"	"		"	"	"	"	OUT						OUT	- "	A to e	-			
		"	124 & 125	5.0 V	5.0 V				5.0 V									OUT		A to f		"	"	
		"	126 & 127	5.0 V	5.0 V				5.0 V								OUT	551		A to g		"		
		1			0.0.																			
		"	128 & 129	GND	GND	"	OUT	GND	GND	"	"									A to RBO				
			128 & 129 130 & 131	GND IN	GND "	"	OUT	GND 5.0 V	GND 5.0 V	" GND	"					OUT			"			"	"	
		"	130 & 131 132 & 133	GND IN	"	"	OUT	5.0 V	5.0 V GND	GND 5.0 V	"					OUT			"	A to RBO B to a B to a	= =	"	"	
		"	130 & 131 132 & 133 134 & 135	GND IN	" 5.0 V	"	OUT		5.0 V GND GND	GND 5.0 V GND					OUT				" " "	A to RBO B to a B to a B to b	"	"	"	
		" " " " " " " " " " " " " " " " " " " "	130 & 131 132 & 133 134 & 135 136 & 137	GND IN	5.0 V GND	"	OUT	5.0 V	5.0 V GND GND 5.0 V	GND 5.0 V GND GND	"								" " " " " " " " " " " " " " " " " " " "	A to RBO B to a B to a B to b B to b	" " " " " " " " " " " " " " " " " " " "	" " "	"	
		" " " " " " " " " " " " " " " " " " " "	130 & 131 132 & 133 134 & 135 136 & 137 138 & 139	GND IN	5.0 V GND 5.0 V	"	OUT	5.0 V	5.0 V GND GND 5.0 V GND	GND 5.0 V GND GND 5.0 V	"				OUT				" " " " " " " " " " " " " " " " " " " "	A to RBO B to a B to a B to b B to b	"	" " " " " " " " " " " " " " " " " " " "	" " "	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141	GND IN	5.0 V GND	"	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V	GND 5.0 V GND GND 5.0 V 5.0 V	"			OUT					" " " " " " " " " " " " " " " " " " " "	A to RBO B to a B to a B to b B to b B to b B to b	" " " " " " " " " " " " " " " " " " " "	" " " " " " " " " " " " " " " " " " " "	" " " " " " " " " " " " " " " " " " " "	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143	GND IN	5.0 V GND 5.0 V	"	OUT	5.0 V	5.0 V GND GND 5.0 V GND	GND 5.0 V GND GND 5.0 V 5.0 V GND	"		OLIT	OUT					11 11 11 11 11 11 11 11 11 11 11 11 11	A to RBO B to a B to a B to b B to b B to b B to b B to c	11 11 11 11 11 11 11 11 11 11 11 11 11	11 11 11 11 11 11 11 11 11 11 11 11 11	# # # # # # # # # # # # # # # # # # #	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145	GND IN	5.0 V GND 5.0 V GND "	"	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V	GND 5.0 V GND GND 5.0 V 5.0 V GND 5.0 V	"		OUT	OUT					11 11 11 11 11 11 11 11 11 11 11 11 11	A to RBO B to a B to a B to b B to c B to c	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	11 11 11 11 11 11 11 11 11 11 11 11 11	H H H H H H H H H H H H H H H H H H H	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147	GND IN	5.0 V GND 5.0 V	"	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V GND	GND 5.0 V GND GND 5.0 V 5.0 V GND 5.0 V GND	"	OUT	OUT	OUT					11 11 11 11 11 11 11 11 11 11 11 11 11	A to RBO B to a B to a B to b B to c B to d B to d	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	H H H H H H H H H H H H H H H H H H H	11 11 11 11 11 11	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147 148 & 149	GND IN "	5.0 V GND 5.0 V GND "	11 11 11 11 11 11 11 11 11	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V GND	GND 5.0 V GND GND 5.0 V 5.0 V GND 5.0 V GND GND	11 11 11 11 11	OUT		OUT				OUT	" " " " " " " " " " " " " " " " " " "	A to RBO B to a B to a B to b B to b B to b B to c B to d B to d B to d B to d	11 11 11 11 11 11 11 11 11 11 11 11 11	11 11 11 11 11 11 11 11 11 11 11 11 11	11 11 11 11 11 11 11 11 11 11 11 11 11	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147	GND IN "	5.0 V GND 5.0 V GND "	11 11 11 11 11 11	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V GND	GND 5.0 V GND GND 5.0 V 5.0 V GND 5.0 V GND	11 11 11 11 11	OUT		OUT				OUT	11 11 11 11 11 11 11 11 11 11 11 11 11	A to RBO B to a B to a B to b B to c B to d B to d	11	11 11 11 11 11 11 11 11 11 11 11 11 11	1	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147 148 & 149 150 & 151	GND IN " " " " " " " " " " " " " " " "	5.0 V GND 5.0 V GND " " 5.0 V	11 11 11 11 11 11 11 11 11 11 11 11 11	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V GND	GND 5.0 V GND 5.0 V 5.0 V GND 5.0 V GND GND GND GND GND GND	11 11 11 11 11 11 11 11 11 11 11 11 11	OUT		OUT				OUT	11 11 11 11 11 11 11 11 11 11 11 11 11	A to RBO B to a B to a B to b B to b B to b B to b B to c B to d B to c B to d	11	11 11 11 11 11 11 11 11 11 11 11 11 11	11 11 11 11	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147 148 & 149 150 & 151 152 & 153	GND IN " " " " " " " " " " " " " " " " " "	5.0 V GND 5.0 V GND " " 5.0 V	11 11 11 11 11 11 11 11 11 11 11 11 11	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V GND ""	GND 5.0 V GND 5.0 V 5.0 V GND 5.0 V GND 5.0 V GND GND TO W	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	OUT		OUT		OUT	OUT	OUT	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	A to RBO B to a B to a B to b B to b B to b B to b B to c B to d B to e B to f B to f	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	11 11 11 11 11 11 11 11 11 11 11 11 11	" " " " " " " " " " " " " " " " " " " "	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147 148 & 149 150 & 151 152 & 153 154 & 155 156 & 157	GND IN	5.0 V GND 5.0 V GND " " 5.0 V	11 11 11 11 11 11 11 11 11 11 11 11 11	OUT	5.0 V	5.0 V GND GND 5.0 V GND """""""""""""""""""""""""""""""""""	GND 5.0 V GND 5.0 V 5.0 V GND 5.0 V GND GND GND GND GND GND	11 11 11 11 11 11 11 11 11 11 11 11 11	OUT		OUT	11		OUT	OUT "	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	A to RBO B to a B to a B to b B to b B to b B to b B to c B to d C to a	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	11 11 11 11	
			130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 146 & 147 148 & 149 150 & 151 152 & 153 154 & 155 156 & 157	GND IN " " " " " " " " " " " " " " " " " "	5.0 V GND 5.0 V GND " " 5.0 V " " GND	11 11 11 11 11 11 11 11 11 11 11 11 11	OUT	5.0 V	5.0 V GND GND 5.0 V GND 5.0 V GND " " " " 5.0 V GND	GND 5.0 V GND 5.0 V 5.0 V GND 5.0 V GND 5.0 V GND GND TO W	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	OUT		OUT		OUT	OUT	OUT	11 11 11 11 11 11 11 11 11 11 11 11 11	A to RBO B to a B to a B to b B to b B to b B to b B to c B to d B to c B to d B to d B to e B to f B to f B to f B to g	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	11 11 11 11 11 11 11 11 11 11 11 11 11	

See footnotes at end of device type 04.

TABLE III. Group A inspection for device type 04 - Continued. 1/

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases <u>1</u> / 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	iits	Unit
			Test no.	В	С	LT	RBO	RBI	D	Α	GND	е	d	С	b	а	g	f	V_{CC}		Min	Max	
9	t _{PLH1}	3003	164 & 165	GND	IN	5.0 V		5.0 V	GND	GND	GND		OUT						5.0 V	C to d	5	105	ns
Tc = 25°C	t _{PHL1}	Fig. 4	166 & 167						GND		"	OUT								C to e			
			168 & 169	5.0 V	"	"		"	5.0 V		"							OUT		C to f	-		
			170 & 171	GND	GND	IN			GND	5.0 V								OUT	"	C to f		"	
		"	172 &173	5.0 V	5.0 V	"		"	IN	5.0 V	"					OUT				D to a			
			174 & 175	GND	5.0 V			"	"	GND	"			OUT					"	D to c		"	"
			176 & 177		GND	"		"	"		"						OUT			D to g	-		
			178 & 179		"	"		GND	GND		"					OUT				LT to a			
			180 & 181		"	"	OUT	GND	"	"	"								"	LT to RBO			
			182 & 183		"	5.0 V	IN	5.0 V			"					OUT			"	RBO to a		"	"
			184 & 185		"	"		IN	"		"					OUT				RBI to a	-		
		"	186 & 187	"	"	"	OUT	"	"	"	"								"	RBI to RBO			"
10	Same to	ests and teri	minal condition	ons as for	subgroup	9, except	$T_{\rm C} = +125$	°C and for	following	limits: t _{PLH}	= 5 to 15	8 ns; t _{PHL}	1 = 5 to 158	ns.						•			
11	Same to	ests, termina	al conditions	and limits	as for sub	group 10,	except T _C	= -55°C															

^{1/} Pins not designated are high \geq 2.0 V; low \leq 0.7 V; or open. Case X and 2 pins not referenced are NC. 2/ Test limits shall be as follows:

Test	Min/Max limits (mA) for circuits									
	Α	Ш	G							
I _{IL2}	12/36	11/35	12/36							
I _{IL3}	36/-1.08	36/1.37	36/-1.08							

 $^{3\!\!/}$ X = Input may be high level or low level. $4\!\!/$ A pullup resistor of 665Ω to 2 k $\!\Omega$ shall be connected between each output and V_{CC}. $5\!\!/$ H \geq 1.5 V; L \leq 1.5 V; $_$ = 2.5 V; B = 0.4 V.

5. PACKAGING

5.1 <u>Packaging requirements</u>. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

- 6.1 <u>Intended use.</u> Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.
 - 6.2 Acquisition requirements. Acquisition documents should specify the following:
 - a. Title, number, and date of the specification.
 - b. Complete part number (see 1.2).
 - c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
 - d. Requirements for certificate of compliance, if applicable.
 - e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
 - f. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
 - g. Requirements for product assurance options.
 - h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
 - j. Requirements for "JAN" marking.
- 6.3 <u>Superseding information.</u> The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.
- 6.4 <u>Qualification</u>. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.

6.5 <u>Abbreviations, symbols, and definitions.</u> The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND	Ground zero voltage potential.
V _{IN}	Voltage level at an input terminal.
V _{IC}	- · · · · · · · · · · · · · · · · · · ·
RBI	Ripple blanking input.
RBO	
BI	

- 6.6 <u>Logistic support.</u> Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming shall not affect the part number.
- 6.7 <u>Substitutability.</u> The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information shall not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device type	Generic-industry type
01	54LS138
02	54LS139
03	54LS42
04	54LS47

6.8 <u>Manufacturers' designation.</u> Manufacturers' circuits, which form a part of this specification, are designated as shown in table IV herein.

TABLE IV. Manufacturer's designator.

	Α	В	С	D	E	F	G
Device	Texas Instru-	Signetics	Raytheon	Fairchild	Motorola	Advanced Micro	National
type	ments Inc.	Corporation	Company	Semiconduct	Inc	Devices	Semiconductor
				or			Corp
01	Х	Х	Χ	X	Х	Χ	X
02	Х	Х	Χ	X	Х	Χ	X
03	X	X		X	X		X
04	Х				Χ		Χ

6.9 <u>Changes from previous issue.</u> Asterisks are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians: Preparing activity: Army - CR DLA - CC

Navy - EC
Air Force - 11 (Project 5962-1955)

DLA - CC

Review activities: Army - HD, MI, SM Navy - AS, CG, MC, SH, TD Air Force - 03, 19, 99