

PART NUMBER 74HC133D-G-ROCV

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

 Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



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SN54HC133, SN74HC133

13-Input Positive-NAND Gates

These devices contain a single 13-input NAND gate. They perform the Boolean functions in positive logic: $Y = \overline{A \cdot B \cdot C \cdot D \cdot E \cdot F \cdot G \cdot H \cdot I \cdot J \cdot K \cdot L \cdot M}$ or $Y = \overline{A \cdot B \cdot C \cdot D \cdot E \cdot F \cdot G \cdot H \cdot I \cdot J \cdot K \cdot L \cdot M}$

The SN54HC133 is characterized for operation over the full military temperature range of -55°C to 125°C while the SN74HC133 is characterized for operation from -40°C to 85°C.

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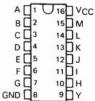
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Quality Overview

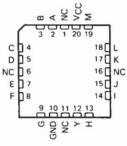
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SN54HC133 . . . FK PACKAGE (TOP VIEW)



NC-No internal connection

description

Reliability

These devices contain a single 13-input NAND gate. They perform the Boolean functions in positive logic:

Package Options Include Plastic "Small

Outline" Packages, Ceramic Chip Carriers,

and Standard Plastic and Ceramic 300-mil

Dependable Texas Instruments Quality and

$$Y = \overline{A \cdot B \cdot C \cdot D \cdot E \cdot F \cdot G \cdot H \cdot I \cdot J \cdot K \cdot L \cdot M}$$
 or

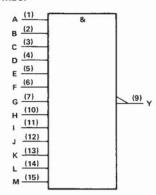
$$Y = \overline{A} + \overline{B} + \overline{C} + \overline{D} + \overline{E} + \overline{F} + \overline{G} + \overline{H} + \overline{I} + \overline{J} + \overline{K} + \overline{L} + \overline{M}$$

The SN54HC133 is characterized for operation over the full military temperature range of $-55\,^{\circ}$ C to 125 °C. The SN74HC133 is characterized for operation from $-40\,^{\circ}$ C to 85 °C.

FUNCTION TABLE

INPUTS A THRU M	OUTPUT
All inputs H	L
One or more inputs L	н

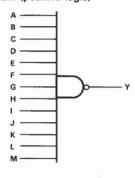
logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range†

Supply voltage, VCC	V to 7 V
Input clamp current, I _{IK} (V _I < 0 or V _I > V _{CC})	± 20 mA
Output clamp current, IOK (VO < 0 or VO > VCC	± 20 mA
Continuous output current, IO (VO = 0 to VCC)	
Continuous current through VCC or GND pins	± 50 mA
Lead temperature 1,6 mm (1/16 in) from case for 60 s: FK or J package	. 300°C
Lead temperature 1,6 mm (1/16 in) from case for 10 s: D or N package	. 260°C
Storage temperature range65°C to	to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

3-1		SN54HC133		33 SN74HC133		SN74HC133		
		MIN	NOM	NOM MAX		NOM	MAX	UNIT
V _{CC} Supply voltage	700 - W - AND - 2005 - 1005	2	5	6	2	5	6	٧
	V _{CC} = 2 V	1.5			1.5		10	
VIH High-level input voltage	V _{CC} = 4.5 V	3.15			3.15			V
	VCC = 6 V	4.2			4.2		100	
	V _{CC} = 2 V	0		0.3	0		0.3	
VIL Low-level input voltage	V _{CC} = 4.5 V	0		0.9	0		0.9	V
	VCC = 6 V	0		1.2	0		1.2	
V _I Input voltage		0		Vcc	0	415.50	Vcc	V
VO Output voltage		0		Vcc	0		Vcc	٧
	V _{CC} = 2 V	0		1000	0		1000	
tt Input transition (rise and fall) times	$V_{CC} = 4.5 V$	0		500	0		500	ns
	V _{CC} = 6 V	0		400	0		400	
TA Operating free-air temperature		-55		125	-40		85	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TECT COMPTIONS	vcc	TA = 25°C			SN54HC133		SN74HC133		
	TEST CONDITIONS		MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
		2 V	1.9	1.998		1.9		1.9		
VOH	$V_{I} = V_{IH}$ or V_{IL} , $I_{OH} = -20 \mu A$	4.5 V	4.4	4.499		4.4		4.4		v
		6 V	5.9	5.999		5.9		5.9		
	$V_I = V_{IH}$ or V_{IL} , $I_{OH} = -4$ mA	4.5 V	3.98	4.30		3.7		3.84	2007 - 2004	
	$V_{l} = V_{IH}$ or V_{IL} , $I_{OH} = -5.2$ mA	6 V	5.48	5.80		5.2		5.34		
VOL	$V_{\parallel} = V_{\parallel H}$ or $V_{\parallel L}$, $I_{OL} = 20~\mu A$	2 V		0.002	0.1		0.1		0.1	v
		4.5 V		0.001	0.1		0.1	1	0.1	
		6 V		0.001	0.1		0.1		0.1	
	V _i = V _{IH} or V _{IL} , I _{OL} = 4 mA	4.5 V		0.17	0.26		0.4		0.33	
	VI = VIH or VIL. IOL = 5.2 mA	6 V		0.15	0.26		0.4		0.33	
lį	VI = VCC or 0	6 V		±0.1	±100		± 1000	1.0	± 1000	nA
lcc	$V_I = V_{CC}$ or 0, $I_O = 0$	6 V			2		40		20	μА
Ci		2 to 6 V		3	10		10		10	pF

switching characteristics over recommended operating free-air temperature range (unless otherwise noted), $C_L=50~\mathrm{pF}$ (see Note 1)

PARAMETER	FROM	то		TA = 25°C		°C	SN54HC133		SN74HC133		
	(INPUT)	(OUTPUT)	Vcc	MIN	TYP	MAX	MIN	MAX	MIN	MAX	רואט
			2 V		70	150		225		190	
t _{pd} Any	Any Y 4.5 V	Y	Y	4.5 V		16	30	45		38	ns
			6 V		13	26		38		33	
			2 V		38	75		110		95	
tt	Y 4.5	4.5 V		8	15		22		19	ns	
			6 V		6	13		19		16	

Cpd	Power dissipation capacitance	No load, TA = 25°C	24 pF typ

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.