

# 2A, 50V - 600V Super Fast Rectifier

#### **FEATURES**

- AEC-Q101 qualified available
- · Glass passivated chip junction
- High efficiency, Low V<sub>F</sub>
- · High current capability
- High surge current capability
- Low power loss
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

#### **MECHANICAL DATA**

Case: DO-204AC (DO-15)

Molding compound meets UL 94V-0 flammability rating

• Terminal: Pure tin plated leads, solderable per J-STD-002

Meet JESD 201 class 2 whisker test

• Polarity: Indicated by cathode band

• Weight: 0.400g (approximately)

KEY PARAMETERS					
PARAMETER	VALUE	UNIT			
I <sub>F</sub>	2	Α			
$V_{RRM}$	50 - 600	V			
I <sub>FSM</sub>	50	Α			
T <sub>J MAX</sub>	150	°C			
Package	DO-204AC (DO-15)				
Configuration	Single die				









ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)										
PARAMETER	SYMBOL	SF 21G	SF 22G	SF 23F	SF 24G	SF 25G	SF 26G	SF 27G	SF 28G	UNIT
Marking code on the device		SF21G	SF22G	SF23F	SF24G	SF25G	SF26G	SF27G	SF28G	
Repetitive peak reverse voltage	$V_{RRM}$	50	100	150	200	300	400	500	600	V
Reverse voltage, total rms value	$V_{R(RMS)}$	35	70	105	140	210	280	350	420	V
Forward current	I <sub>F</sub>	2						Α		
Surge peak forward current, 8.3ms single half sine wave superimposed on rated load	I <sub>FSM</sub>	50					А			
Junction temperature	$T_J$	-55 to +150					°C			
Storage temperature	T <sub>STG</sub>	-55 to +150					°C			





THERMAL PERFORMANCE						
PARAMETER	SYMBOL	TYP	UNIT			
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	65	°C/W			
Junction-to-case thermal resistance	R <sub>eJC</sub>	16	°C/W			

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage <sup>(1)</sup>	SF21G SF22G SF23G SF24G	- I <sub>F</sub> = 2A, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	0.95	V
	SF25G SF26G			-	1.30	V
	SF27G SF28G			-	1.70	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>		T <sub>J</sub> = 25°C		-	5	μΑ
		T <sub>J</sub> = 125°C	- I <sub>R</sub>	-	100	μΑ
lunction capacitance	SF21G SF22G SF23G SF24G	1MHz, V <sub>R</sub> = 4.0V		40	-	pF
Junction capacitance	SF25G SF26G SF27G SF28G		CJ	20	-	pF
Reverse recovery time		$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t <sub>rr</sub>	-	35	ns

#### Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION					
ORDERING CODE <sup>(1)(2)</sup>	PACKAGE	PACKING			
SF2xG	DO-204AC (DO-15)	3,500 / Tape & Reel			
SF2xG A0G	DO-204AC (DO-15)	1,500 / Ammo box			
SF2xGH	DO-204AC (DO-15)	3,500 / Tape & Reel			
SF2xGHA0G	DO-204AC (DO-15)	1,500 / Ammo box			

#### Notes:

- 1. "x" defines voltage from 50V (SF21G) to 600V (SF28G)
- 2. "H" means AEC-Q101 qualified



#### **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

**Fig.1 Forward Current Derating Curve** 

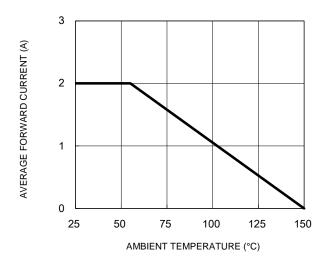


Fig.3 Typical Reverse Characteristics

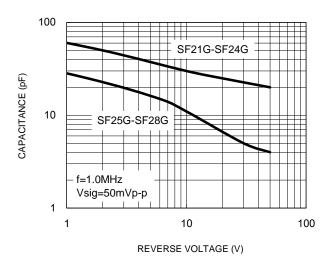
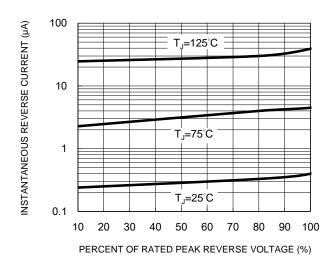


Fig.2 Typical Junction Capacitance

Fig.4 Typical Forward Characteristics



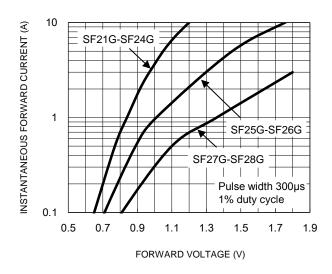
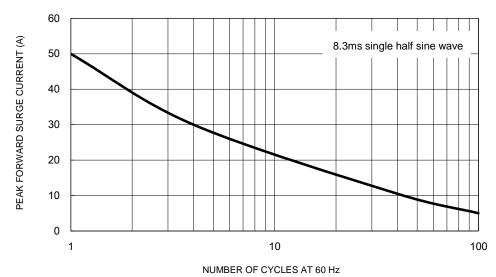


Fig.5 Maximum Non-Repetitive Forward Surge Current



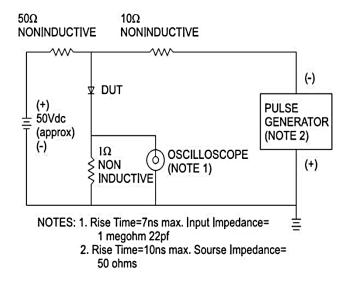


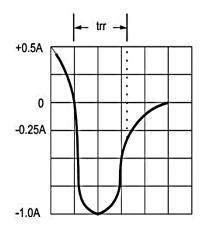


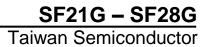
### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram



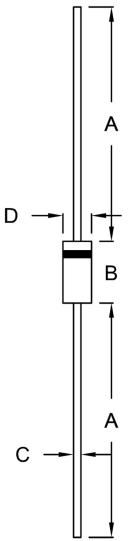






## **PACKAGE OUTLINE DIMENSIONS**





DIM. Uni		(mm)	Unit (inch)		
	Min.	Max.	Min.	Max.	
А	25.40	-	1.000	-	
В	5.80	7.60	0.228	0.299	
С	0.70	0.90	0.028	0.035	
D	2.60	3.60	0.102	0.142	

## **MARKING DIAGRAM**



= Marking Code P/N G = Green Compound

YWW = Date Code = Factory Code F



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