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## CCD SIGNAL PROCESSOR FOR SCANNER APPLICATIONS

## **FEATURES**

- INTEGRATED TRIPLE-CORRELATED DOUBLE SAMPLER
- OPERATION MODE SELECTABLE:
  1-Channel, 3-Channel CCD Mode, 8Msps
- PROGRAMMABLE GAIN AMPLIFIER: 0dB to +13dB
- SELECTABLE OUTPUT MODES: Normal/Demultiplexed
- OFFSET CONTROL RANGE: ±500mV
- +3V, +5V Digital Output
- LOW POWER: 300mW (typ)
- **LQFP-48 SURFACE-MOUNT PACKAGE**

## DESCRIPTION

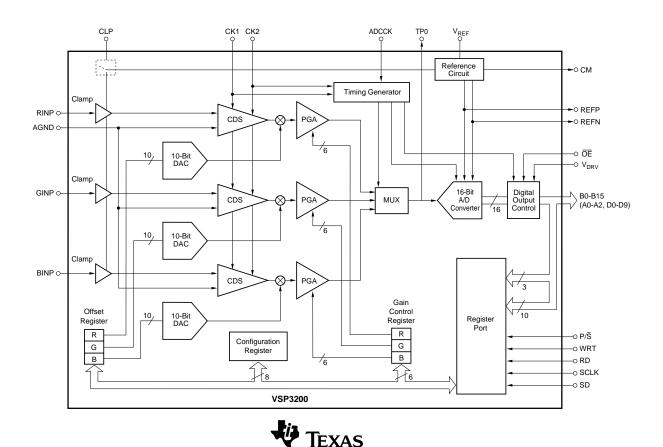
The VSP3200 and VSP3210 are complete CCD image processors that operate from single +5V supplies.

This complete image processor includes three Correlated Double Samplers (CDSs) and Programmable Gain Amplifiers (PGAs) to process CCD signals.

The VSP3200 is interface compatible with the VSP3210, which is a 16-bit, one-chip product.

The VSP3210 is pin-to-pin compatible with VSP3100, when in demultiplexed output mode.

The VSP3200 and VSP3210 can be operated from 0°C to +85°C, and are available in LQFP-48 packages.



INSTRUMENTS

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