COMPLIANT

HALOGEN FREE

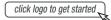


Vishay General Semiconductor

High Current Density Surface Mount Schottky Barrier Rectifiers



DESIGN SUPPORT TOOLS





PRIMARY CHARACTERISTICS				
I _{F(AV)}	5.0 A			
V _{RRM}	30 V, 40 V			
I _{FSM}	150 A			
E _{AS}	20 mJ			
V_F at $I_F = 5.0$ A	0.403 V			
T _J max.	150 °C			
Package	ckage SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · High efficiency
- · Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available -Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3_X - halogen-free, RoHS-compliant, and automotive grade ("_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SS5P3	SS5P4	UNIT	
Device marking code		S53	S54		
Maximum repetitive peak reverse voltage	V _{RRM}	30	40	V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)}	5.0		Α	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	150		А	
Non-repetitive avalanche energy at I _{AS} = 2.0 A, T _J = 25 °C	E _{AS}	20		mJ	
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150		°C	



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage	$I_F = 2.5 A$	T _A = 25 °C	V _F ⁽¹⁾	0.416	-	V
	I _F = 5.0 A			0.476	0.52	
	I _F = 2.5 A	T _A = 125 °C		0.312	-	
	I _F = 5.0 A			0.403	0.45	
Maximum reverse current	Rated V _R	T _A = 25 °C	I _R ⁽²⁾	61.8	250	μΑ
	nated v _R	T _A = 125 °C		26.7	40	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	280	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	SS5P3 SS5P4		UNIT		
Typical thermal resistance	R _{θJA} (1)	60		°C/W		
Typical trieffial resistance	$R_{ heta JL}$	3				

Note

(1) Units mounted on recommended PCB 1 oz. pad layout

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SS5P4-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel		
SS5P4-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel		
SS5P4HM3_A/H (1)	0.10	Н	1500	7" diameter plastic tape and reel		
SS5P4HM3_A/I (1)	0.10	I	6500	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise specified)

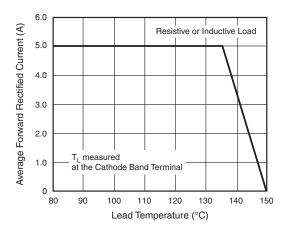


Fig. 1 - Maximum Forward Current Derating Curve

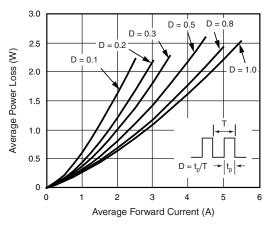


Fig. 2 - Forward Power Loss Characteristics

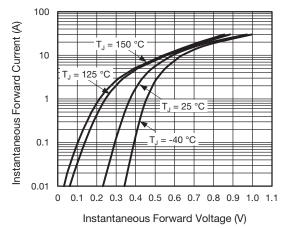


Fig. 3 - Typical Instantaneous Forward Characteristics

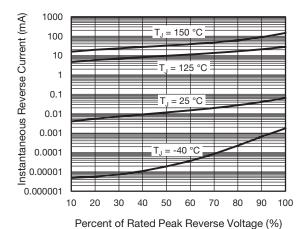


Fig. 4 - Typical Reverse Leakage Characteristics

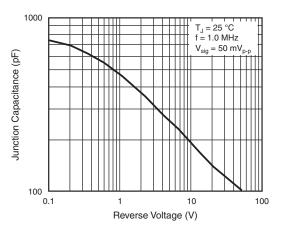


Fig. 5 - Typical Junction Capacitance

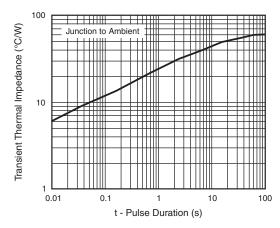
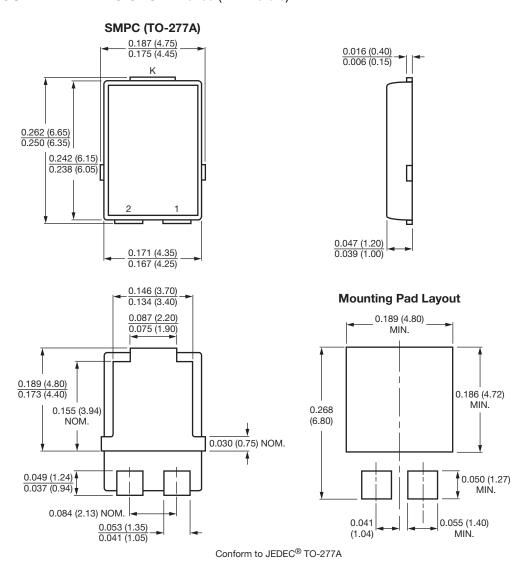


Fig. 6 - Typical Transient Thermal Impedance



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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