VS-43CTQ...S-M3, VS-43CTQ...-1-M3 Series

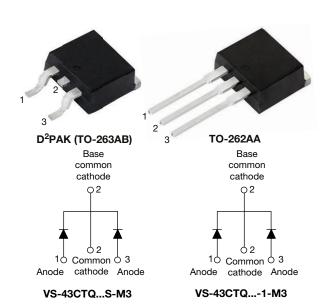
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COMPLIANT

HALOGEN

FREE

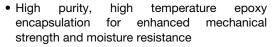
High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS						
I _{F(AV)}	2 x 20 A					
V_{R}	80 V, 100 V					
V _F at I _F	0.67 V					
I _{RM} max.	11 mA at 125 °C					
T _J max.	175 °C					
E _{AS}	7.50 mJ					
Package	D ² PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop



- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	40	Α				
V _{RRM}		80/100	V				
I _{FSM}	$t_p = 5 \mu s sine$	850	Α				
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.67	V				
TJ	Range	-55 to +175	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-43CTQ080S-M3 VS-43CTQ080-1-M3	VS-43CTQ100S-M3 VS-43CTQ100-1-M3	UNITS		
Maximum DC reverse voltage	V_{R}	80	100	V		
Maximum working peak reverse voltage	V_{RWM}	- 60	100	V		



VS-43CTQ...S-M3, VS-43CTQ...-1-M3 Series

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ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average per leg forward current See fig. 5 per device					20			
		I _{F(AV)}	50 % duty cycle at T _C = 135 °C, rectangular waveform		40	^		
	Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse Following any		850	Α		
surge current per leg See fig. 7	surge current per leg See fig. 7		10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	275			
Non-repetitive avalanche	Non-repetitive avalanche energy per leg		$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.50 \text{A}, L = 60 \text{mH}$		7.50	mJ		
Repetitive avalanche curr	ent per leg	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	Α		

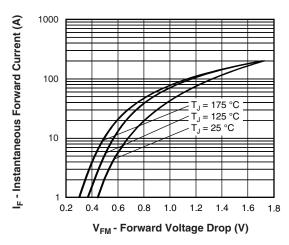
ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		20 A	T _{.1} = 25 °C	0.81	V	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	11 = 23 0	0.98		
	VFM (*)	20 A	T _{.1} = 125 °C	0.67		
		40 A	1j = 125 C	0.81		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1	mA	
See fig. 2		T _J = 125 °C	V _R = nateu V _R	11		
Threshold voltage	V _{F(TO)}	T T mayimum		0.71	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum		0.43	mΩ	
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range	e 100 kHz to 1 MHz), 25 °C	1480	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mr	Measured lead to lead 5 mm from package body			
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

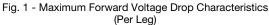
 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHA	NICAL SPI	ECIFICAT	IONS			
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 175	°C	
Maximum thermal resistance, junction to case per leg		р	DC operation	2.0		
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque	Mounting torque maximum			12 (10)	(lbf · in)	
Marking device			Coop of the D2DAY (TO 262AB)	43CTC	2080S	
			Case style D ² PAK (TO-263AB)	43CTQ100S		
warking device	Marking device		Consist do TO 262AA	43CTQ080-1		
			Case style TO-262AA	43CTQ100-1		

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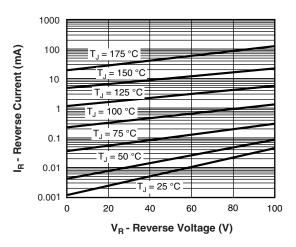


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

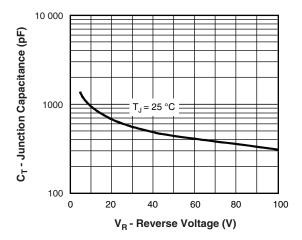


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

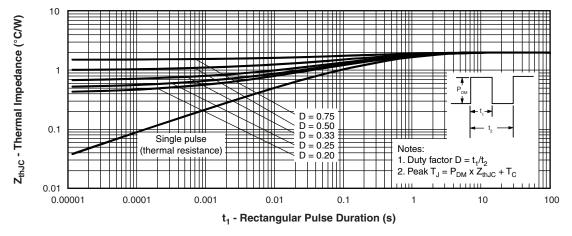


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

Allowable Case Temperature (°C)

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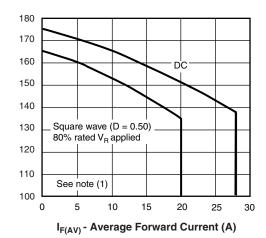


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

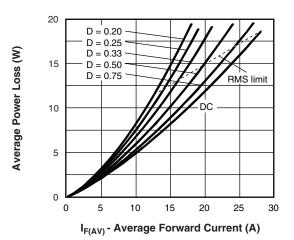


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

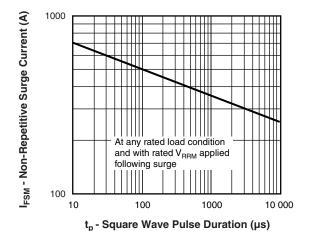


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

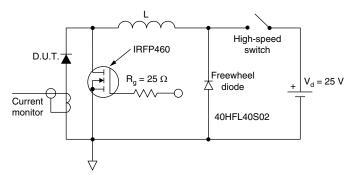


Fig. 8 - Unclamped Inductive Test Circuit

Note

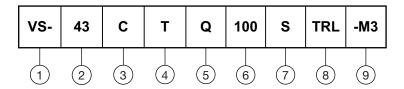
 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = <math display="inline">V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 10 \ V$

VS-43CTQ...S-M3, VS-43CTQ...-1-M3 Series

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 A)

3 - Circuit configuration: C = common cathode

4 - T = TO-220

5 - Schottky "Q" series

6 - Voltage ratings - 080 = 80 V 100 = 100 V

7 - • S = D^2 PAK (TO-263AB)

• -1 = TO-262AA

None = tube

• TRL = tape and reel (left oriented - for D²PAK (TO-263AB) only)

• TRR = tape and reel (right oriented - for D²PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION							
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION					
VS-43CTQ100S-M3	50	Antistatic plastic tubes					
VS-43CTQ100STRL-M3	800	13" diameter plastic tape and reel					
VS-43CTQ100STRR-M3	800	13" diameter plastic tape and reel					
VS-43CTQ100-1-M3	50	Antistatic plastic tubes					

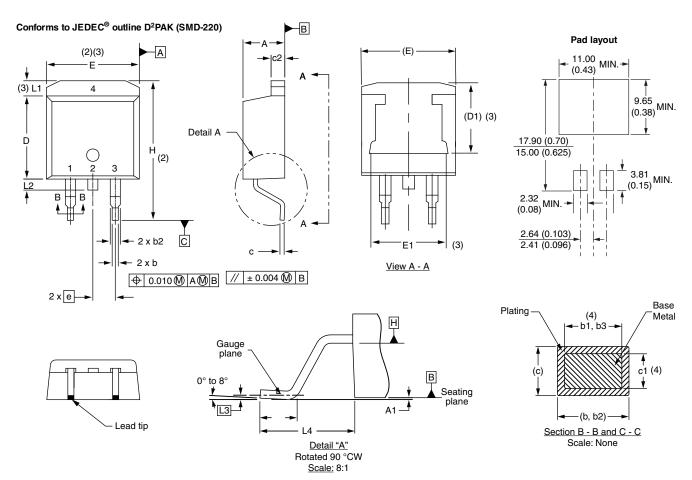
LINKS TO RELATED DOCUMENTS							
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164					
Differsions	TO-262AA	www.vishay.com/doc?96165					
Deut according information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information		www.vishay.com/doc?96424					
SPICE model		www.vishay.com/doc?95065					



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	HES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 13-Jul-17 Document Number: 96164

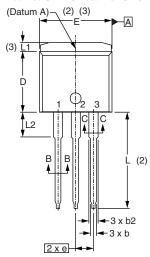


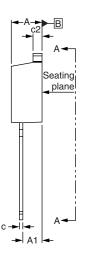
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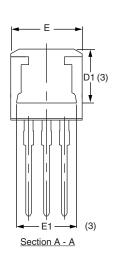
TO-262AA

DIMENSIONS in millimeters and inches

Modified JEDEC® outline TO-262







⊕ 0.010 **M** A**M** B

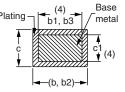
Lead assignments



Diodes 1. - Anode (two die)/open (one die)

2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

	I		T		
SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
 (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- Controlling dimension: inches
- Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)



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